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TOWARDS THE INTELLIGENT HOUSE

The rapid growth of electronics inspires us to take a look at what changes we are likely to see in domestic environments of the future.

Homes will be equipped increasingly with a domestic computer terminal (put in by the builders like a sink unit). This will control the central heating, hot water supply, cooker, video, lighting, and so on. Eventually, there will be a fully automated kitchen that will carry out most of the irksome tasks like ironing and washing up.

This domestic computer will be controlled via the public telephone network. The conventional telephone will be replaced by a wristwatch type, so that the home can be controlled from wherever you are.

The conventional door lock will disappear and be replaced by tone-detecting electronic locks that respond to the householders’ voices.

Although the combustion-engine-driven car will not disappear for a long time to come, there will be an increasing number of electric cars. New, small, large-capacity batteries will make these a commercially viable proposition. All cars will be fitted with a large number of electronic gadgets to take the tediousness out of driving. They will have microprocessors that control fuel injection, gear changing, spring rate, vehicle height, shock absorber damping, and others. All cars will be equipped with anti-brake-lock systems and sensors that actuate the braking-system when you get too close to the car in front.

Increasingly, shopping will be done from home with the aid of videophones and electronic fund transfer.

Home entertainment will be based on digital equipment, and probably be interactive, allowing subscriber selection of high-definition, 3D, large-screen, video, television, and music via common networks.

Television and video communications will dominate the home even more than they do now. With more and more satellites hovering above the equator, signals from them will be received via dishes not much larger than a dinner plate. The screens will be linked in with the telephone network so that all communications will be face-to-face.

Cellular radio systems, linked world-wide by satellite systems will be commonplace so that anyone can communicate with anybody else wherever they may be.

Paraplegics may be able to walk again with the aid of electrical stimulation of their muscles. These stimuli will come from pressure, angular, and acceleration sensors on their limbs.

The deal will have portable videophones in which a microprocessor displays the incoming telephone speech on to a screen.

Electronic devices will continue to get smaller and faster, although the size of finished products will, of course, still be dictated by the needs of the user. Slowly but surely, silicon ICs will be replaced by gallium-arsenide chips, and these, in turn, will be superseded by neural or optical devices. The density of these devices will be staggering by current standards.

The future certainly looks exciting, the more so for those of us who play an active part in the wonderful world of electronics!
BUS INTERFACE FOR HIGH-RESOLUTION LIQUID CRYSTAL SCREENS

Part 2

Construction
The LC screen interface is constructed on a double-sided, through-plated printed circuit board (see Fig. 4). The track layout is not given here because this PCB is virtually impossible to make other than from films, while through-plating equipment is usually only available in a professional workshop. The size of the ready-made PCB is such that it can be attached to the controller board of the LM4001 unit with the aid of 4 spacers. The connection between the interface board and the existing controller board is conveniently made in a short length of 10-way flat ribbon cable. The mounting of the standard-sized components on the board should not present difficulties. Only the controller, ICs, deserves special attention: This IC is housed in a 64-pin flatpack enclosure for surface mounting, with pins in a 1 mm, rather than a 0.1 in., raster. Use a low-power soldering iron with a small tip to solder the terminal pins of the controller direct on to the relevant copper tracks. Work very carefully, and use desoldering braid to remove solder when a short-circuit is made between adjacent pins. As to orientation of the controller chip on the board, stick to the component overlay, because pin 1 is not located immediately next to the bevelled edge of the enclosure!

Connector K1, a 40-way PCB header with eject handles, is secured on to the PCB by means of two small bolts and nuts. The pinning of K1 makes it possible to use a direct, pin-to-pin, connection, in flatcable, to the expansion connector on the BASIC computer 10. For other computer systems, it is necessary to provide a do-it-yourself connection between K1 and the bus (see Tables 3a and 3b). Whatever connection is used, the total length of the cable between the computer bus and K1 should not exceed 30 cm or so.

Programming the LC screen interface
Software for producing ASCII characters on the LC screen is relatively simple to produce thanks to the con-

Table 2

<table>
<thead>
<tr>
<th>Processor</th>
<th>Z80* (MSX)</th>
<th>6502</th>
<th>IBM-PC*</th>
<th>ELEKTOR BASIC COMPUTER</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mapping</td>
<td>I/O</td>
<td>MEMORY</td>
<td>I/O</td>
<td>MEMORY</td>
</tr>
<tr>
<td>k1</td>
<td>X0</td>
<td>S1</td>
<td>M1</td>
<td>off</td>
</tr>
<tr>
<td></td>
<td>X1</td>
<td>S2</td>
<td>--</td>
<td>off</td>
</tr>
<tr>
<td></td>
<td>X2</td>
<td>S3</td>
<td>--</td>
<td>off</td>
</tr>
<tr>
<td></td>
<td>A3-A6</td>
<td>54-56</td>
<td>A3-A6</td>
<td>X</td>
</tr>
<tr>
<td></td>
<td>A6-A7</td>
<td>57-58</td>
<td>A6-A7</td>
<td>X</td>
</tr>
<tr>
<td></td>
<td>A9-A15</td>
<td>59-516</td>
<td>--</td>
<td>(MSX ON)</td>
</tr>
</tbody>
</table>

Address range: MSX 0-3FH, system dependent 300H-31FH, system dependent

*IC6, S9-S16 and R2 may be omitted

![Diagram](image)

Fig. 6. Difference between character (text) display and graphic display mode as regards processing of individual bits loaded from the screen RAM.
Fig. 5. Bit assignment in the MODE register of the LCD controller.

Table 3a

<table>
<thead>
<tr>
<th>SLOT</th>
<th>SIGNAL DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>PIN NO.</td>
<td>I/O</td>
</tr>
<tr>
<td>CS1</td>
<td>O ROM 4000 ~ 7FFF select signal (128K)</td>
</tr>
<tr>
<td>CS2</td>
<td>O ROM 8000 ~ BFFF select signal (128K)</td>
</tr>
<tr>
<td>CS12</td>
<td>O ROM 4000 ~ 7FFF select signal (256K)</td>
</tr>
<tr>
<td>SLTSL</td>
<td>O Slot select signal, fixed select signal for each slot, Reserved for future use</td>
</tr>
<tr>
<td>RFSH</td>
<td>O Refresh signal</td>
</tr>
<tr>
<td>WAIT</td>
<td>I Wait for signal to CPU (wired-OR)</td>
</tr>
<tr>
<td>INT</td>
<td>I Interrupt request signal</td>
</tr>
<tr>
<td>M1</td>
<td>O Fetch cycle signal of CPU</td>
</tr>
<tr>
<td>BUSDIR</td>
<td>I This signal controls the direction of external data bus buffer when the cartridge is selected. It is low when the data is sent by the cartridge.</td>
</tr>
<tr>
<td>IOG</td>
<td>O I/O request signal</td>
</tr>
<tr>
<td>MERQ</td>
<td>O Memory request signal</td>
</tr>
<tr>
<td>WR</td>
<td>O Write signal</td>
</tr>
<tr>
<td>RD</td>
<td>O Read signal</td>
</tr>
<tr>
<td>RESET</td>
<td>O System reset signal</td>
</tr>
<tr>
<td>A8</td>
<td>O Reserved for future use</td>
</tr>
<tr>
<td>A15</td>
<td>O</td>
</tr>
<tr>
<td>A11</td>
<td>O</td>
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<td>A10</td>
<td>O</td>
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<td>A7</td>
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<td>A6</td>
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<td>A12</td>
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<td>A14</td>
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<td>A5</td>
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<tr>
<td>D1</td>
<td>I/O</td>
</tr>
<tr>
<td>D0</td>
<td>I/O</td>
</tr>
<tr>
<td>D4</td>
<td>I/O</td>
</tr>
<tr>
<td>D2</td>
<td>I/O</td>
</tr>
<tr>
<td>D6</td>
<td>I/O</td>
</tr>
<tr>
<td>D4</td>
<td>I/O</td>
</tr>
<tr>
<td>D7</td>
<td>I/O</td>
</tr>
<tr>
<td>D6</td>
<td>I/O</td>
</tr>
<tr>
<td>GND</td>
<td>-</td>
</tr>
<tr>
<td>CLOCK</td>
<td>0</td>
</tr>
<tr>
<td>SW1, SW2</td>
<td>5V</td>
</tr>
<tr>
<td>5V, 47</td>
<td>+12V power supply</td>
</tr>
<tr>
<td>49, 48</td>
<td>Sound input (1 - 5 dBm)</td>
</tr>
<tr>
<td>50</td>
<td>-12V power supply</td>
</tr>
</tbody>
</table>

Input and output refers to MSX computer

Table 3b Signal functions on IBM bus

<table>
<thead>
<tr>
<th>Signal name</th>
<th>Signal Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>A1 - 10CH.2</td>
</tr>
<tr>
<td>+RESET DRY</td>
<td>A2 - D7.0</td>
</tr>
<tr>
<td>+5V</td>
<td>A3 + D6.0</td>
</tr>
<tr>
<td>+IRQ2</td>
<td>A4 + D5.0</td>
</tr>
<tr>
<td>- 5VDC</td>
<td>A5 + D4.0</td>
</tr>
<tr>
<td>+IRQ2</td>
<td>A6 + D3.0</td>
</tr>
<tr>
<td>- 12V</td>
<td>A7 + D2.0</td>
</tr>
<tr>
<td>Reserved</td>
<td>A8 + D1.0</td>
</tr>
<tr>
<td>+12V</td>
<td>A9 + D0.0</td>
</tr>
<tr>
<td>GND</td>
<td>A10 + 10CH.3</td>
</tr>
<tr>
<td>MEMW</td>
<td>A11 + A5.0</td>
</tr>
<tr>
<td>MEMR</td>
<td>A12 + A4.0</td>
</tr>
<tr>
<td>+K1W</td>
<td>A13 + A3.0</td>
</tr>
<tr>
<td>+K1R</td>
<td>A14 + A2.0</td>
</tr>
<tr>
<td>+DAC13</td>
<td>A15 + A1.0</td>
</tr>
<tr>
<td>+DAC9</td>
<td>A16 + A0.0</td>
</tr>
<tr>
<td>+DACK1</td>
<td>A17 + A12.0</td>
</tr>
<tr>
<td>+DRG1</td>
<td>A18 + A13.0</td>
</tr>
<tr>
<td>+DAC9O</td>
<td>A19 + A12.0</td>
</tr>
<tr>
<td>CLOCK</td>
<td>A20 + A11.1</td>
</tr>
<tr>
<td>+IRQ7</td>
<td>A21 + A10.1</td>
</tr>
<tr>
<td>+IRQ8</td>
<td>A22 + A9.1</td>
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<td>+IRQ9</td>
<td>A23 + A8.1</td>
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<td>+IRQ10</td>
<td>A24 + A7.1</td>
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<tr>
<td>+DC2</td>
<td>A25 + A6.1</td>
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<tr>
<td>+TJC</td>
<td>A26 + A5.1</td>
</tr>
<tr>
<td>+4L</td>
<td>A27 + A4.1</td>
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<tr>
<td>+ALE</td>
<td>A28 + A3.1</td>
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<tr>
<td>+5V</td>
<td>A29 + A2.1</td>
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<tr>
<td>+OSC</td>
<td>A30 + A1.1</td>
</tr>
<tr>
<td>+GND</td>
<td>A31 + A0.1</td>
</tr>
</tbody>
</table>

Table 3b Table 3b Signal functions on IBM bus

Controller taking over the task of generating the dot patterns for the characters. Briefly recapitulating what has been said in the above description of the circuit, the five registers of Table 1 are either read or write locations. Four of these registers control the HD68130B, and one, LATCH, which determines the contrast (bits 0 to 3), the selected 4 Kbyte screen RAM (bit 6), and the selected add-on character font (bit 7).

Table 4 shows that the controller chip offers quite a few programmable features. Its basic operation will be discussed with a few examples as guidance for further developments.

To start with, it is seen that the chip has 14 registers for storing different parameters. One register, number 14, returns the busy flag, which is logic high for about 15 μs after receipt of a controller command. The controller cannot handle a new command as long as the busy flag is active. Busy can be read from the database via register CTRL-0D (control read). It will be clear that there is very little point in using this flag in BASIC, because the relatively low processing speed of this programming language makes it impossible anyway to send a new command to the controller before this has deactivated the busy flag.

Machine code programmers, however, are well advised to have the control program read and process the busy flag.

Before any character can be displayed on the LC screen, the controller must be ini-
For the following description it is assumed that an LC screen Type LM40001 is used. For other types, the relevant data sheets should be examined to analyse the register assignment. The first 4 registers in the HD61830B should always be loaded. Table 4 shows that register 0 is the mode control. The various options available are given in Fig. 5. Writing to a register is done in two passes: first, load the register number in address control-write (CTRLWR), then write the relevant data to address-data-write (DATA-WR). The BASIC listing of Fig. 9 illustrates this procedure. The subroutine starting at line 1000 loads variable DA in register CTL. The other four registers are loaded in a similar fashion.

Lines 60 to 100 in the demonstration program hold the the data for loading controller registers R0 up to and including R4. The corresponding screen settings form a usable default configuration, and are best copied for initial experiments in programming the LC screen. It is possible to read the data at the cursor address. To do this, first load the required cursor address in register 7 (LS byte) and register 8 (MS byte). Then perform a dummy read via address DATA-RD. Next, read the data 'underneath' the cursor from address DATA-RD. Any subsequent read command returns the data at the next address in the screen memory. A new dummy read operation is not required until the cursor address is altered by the control program.

When the LC screen is set to graphics mode, all graphics data to be displayed corresponds to dot information written into the screen memory. The controller is switched to graphics mode by programming a logic 1 for bit 1 in the mode register. The graphics information can then be written direct to the screen memory. Data can be loaded as separate bytes after loading the start and cursor address, similar to the procedure followed in the text mode. Before sending the data byte it is, however, necessary to call register 12 via CTRL-WR, and then write the data to DATA-WR. The dot usage of the controller is shown in Fig. 6. The listing of Fig. 9 may also help to analyse the operation of the graphics mode in further detail. Like ASCII characters, dot information can be read back from the display — write 13 to CTRL-WR, then perform a dummy.

Fig. 7. Illustrating the compiling of a dot pattern matrix to be loaded into an add-on font EPROM.

<table>
<thead>
<tr>
<th>A10</th>
<th>A9</th>
<th>A8</th>
<th>A7</th>
<th>A6</th>
<th>A5</th>
<th>A4</th>
<th>A3</th>
<th>A2</th>
<th>A1</th>
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<tbody>
<tr>
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Table 4

<table>
<thead>
<tr>
<th>Register</th>
<th>HD61830B Register Overview</th>
<th>Data</th>
</tr>
</thead>
<tbody>
<tr>
<td>R/W</td>
<td>RS</td>
<td>DB7~DB4</td>
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<td>0</td>
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</tr>
<tr>
<td>14</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

1.26 Industry, 1989
Adding a character set

As already stated, the controller can use data in an external EPROM to form an additional character set. Figure 7 shows how the controller converts EPROM data into dot patterns on the LC backplane. Using the information given in the figure, a simple computer program may be written to compile a user-defined character table in the EPROM. Alternatively, build the table manually by drawing the character outlines on squared paper. A ready-programmed EPROM with two additional character sets is available as stated in the Parts List.

Fig. 8. Completed interface board fitted on to a Sharp LM40001 LC screen module.

Fig. 9. Graphics demonstration program for the Elektor Electronics BASIC computer plus LC screen interface described here (LM40001). The program runs as line 290 — type CONT to continue the graphics demo. XBY(...) is an output instruction, and ** stands for mathematical squaring.
PITCH CONTROL FOR CD PLAYERS

In general, only professional compact-disc players are provided with a pitch control. Domestic types so equipped are few and far between, and are also pretty expensive. A circuit is described here that makes it possible for a pitch control to be added to most CD players at a fraction of the cost of a professional unit.

Correct operation of a CD player is ensured by a central, crystal-controlled clock operating at 11.2896 MHz. In the block diagram of a typical CD player—see Fig. 1—this clock is contained in the digital filter chip (SAA7220), but the crystal is external to this IC. The clock controls not only the data processing, such as decoding, error correction, and digital-to-analogue conversion, but also the drive motors.

In CD players less sophisticated than the Philips CD960 (used for Fig. 1), a digital filter is not used and the crystal is connected to the XTAL inputs of the decoder chip (here a Type SA7210). For the present purposes, it is fortunate that all the circuits of a CD player continue to operate correctly if the clock frequency is altered, although the motors will run faster or slower, depending on whether the frequency is increased or reduced. In principle, therefore, it is fairly simple to alter the speed of the disc drive motor, and thus the pitch of the sound output.

According to most manufacturers, the clock frequency should be within \( \pm 10\% \) of the nominal value, but trials in a number of CD players have shown that much greater tolerances are permissible. At very large deviations, however, some special functions, such as skip and search, fail to operate correctly. In the proposed circuit, the clock frequency may be varied between 9 MHz and 13 MHz without any detrimental effects on the electronic circuits in the player. Basically, all that is required is to remove (unsolder) the crystal from the appropriate printed-circuit board in the CD player and replace it by the coaxial cable.

Fig. 1. Block diagram of typical CD player (Philips CD960).
PLL synthesizer

In professional CD players fitted with pitch control, the variable clock frequency is derived from a simple, free-running voltage-controlled oscillator—VCO—in which the voltage is varied with the aid of a potentiometer, as shown in Fig. 3. When the VCO is in circuit, the frequency, and thus the speed of the disc drive motor, may be altered by turning or sliding the potentiometer. Note that this circuit is provided with a switch that allows instantaneous return to the original crystal frequency when required.

This type of circuit has some drawbacks, however: owing to temperature drift, the VCO is not very stable; and the speed variation can not be controlled accurately because of the lack of an indicator. The proposed circuit, therefore, has been enlarged and enhanced as may be seen from its block diagram in Fig. 4 and its circuit diagram in Fig. 5.

The circuit is based on a phase-locked-loop (PLL) synthesizer. The reference oscillator of the synthesizer is driven by the crystal removed from the CD player. The frequency of the VCO is compared constantly with that of the reference oscillator and made to keep in step with it. This is effected by dividing the reference signal by 400 and the VCO signal by a factor of between 320 and 460. Any deviation of the VCO frequency results in an appropriate correction in the phase comparator. A LED lights when the PLL is not locked. With the PLL locked, operation of the CD player is just as accurate.
Fig. 5. Circuit diagram of the pitch control unit.

1.30 elektor indt January 1985
and stable as before the crystal was removed from its original position.

Even when the PLL is not locked, however (indicated by a LED lighting), nothing detrimental happens; the VCO then operates in a free-running mode.

The programmable divider in the feedback loop of the VCO is set with the aid of miniature pushbutton switches that control an 8-bit up-down counter. The output data of the counter may vary the divide factor of counter ICs between 160 and 230.

The up-down counter is also connected to EPROM ICs. This circuit is used as a decoder driving a three-digit display. The binary output of the up-down counter is converted into 0.5% steps on the display: 11001000 represents 00.0%. Starting from a counter output of 11001000 (decimal 200), each change of 1 bit (more or less) causes a display shift of 0.5%.

The EPROM also limits the frequency shift to -20% and +15%, because bit 6 (D6) at its output is fed back to block the up-down counter.

The EPROM also provides polarity indication: when the up-down counter output decreases, diode D1 lights to show the minus sign.

Since the EPROM content is divided into three, a Type 4017 IC is used for multiplexing the three display segments. Apart from main dividers ICs and IC13, there are two bistables in IC14 that serve as binary scaler. These dividers ensure that the phase comparator is provided with true square-wave pulses to prevent any problems in the phase comparision. The circuit of IC12 is shown in Fig. 6. The time constant of network R2-Rs-C11 at pin 13 determines the regulating time of the PLL. The regulating voltage is applied to double variable-capacitance diode D1 in the VCO circuit.

The frequency of the VCO is determined by C1-C12-C10-D3. The oscillator is basically the same as the original crystal oscillator.

The oscillator signal is fed via inverter Ne to the output terminal and also to divider IC1. The potential divider at the output, Rz-Rs, provides level matching and forms a low-pass filter with the capacitance of the coaxial cable and the capacitor at the XIN terminal of the SAA7220 in the CD player. Both these measures ensure that the signal at pin 11 of the SAA7220 is a true sine wave at a level of about 1 Vpp.

**Practical considerations**

A phase-locked loop synthesizer on CMOS ICs and operating over the range 9-13 MHz can be constructed properly only on the carefully designed PCB shown in Fig. 7. It is essential that the supply lines are decoupled properly as, for instance, those to the VCO by Rn and Cn.

Since the pitch control circuit draws up to 220 mA, it will normally not be possible to take the power supply from the CD player. A simple +5 V supply will do, however.

Note that because of the high frequencies the dividers in the PLL should be HC or HCT CMOS types; all other ICs may be standard CMOS.

The simple content of the EPROM is given in Fig. 9 to enable constructors to program this device themselves.

Coil L1 consists of 16 turns enameled copper wire of 0.2 mm diameter on a Neosil Type 7FS former. The ends of the winding are soldered to two of the five pins on the base of the former, which themselves are soldered to the PCB.

The inductor is trimmed with the aid of a non-conducting trimming tool. The core is situated correctly if UNLOCK diode D3 does not light at the extremes of the frequency range (+15% and -20%).

It is best, however, to trim the inductor with the aid of a frequency counter. It is then possible to make the readings on the 3-digit display (in %) and on the counter (in MHz) equal. If the PLL is not locked properly, the reading on the counter becomes unstable and D2 will light.

With L1 trimmed correctly, the regulating voltage at pin 13 of the phase comparator must be about 0.5 V at +15% frequency shift, and around 4.0 V at -20%.

It is also possible, if a frequency counter...
is not available, to measure the voltage across D2. Since that is a pulse-width modulated signal, however, an integrating multimeter must be used, set to the DC range. At both extremes of the frequency shift, the d.c. voltage across D2 must be not greater than 150 mV.

If the varicap diode, D3, is out of tolerance, so that the correct frequency shift can not be obtained, the values of C7 and C8 may be changed slightly (smaller value = higher VCO frequency). In extreme cases, it may be necessary to increase the number of turns on L1 to 18 or even 19.

---

**Parts list**

- **Resistors (± 5%):**
  - R1, R6, R9, R12...R14 incl. = 1kΩ
  - R2, R5, R8, R15 = 100k
  - R13, R14 = 1MΩ
  - R7, R16 = 10k
  - R8 = 1k6
  - R10, R13 = 270R
  - R18 = 47k
  - R19 = 100k
  - R21 = 47k
  - R22 = 82k
  - R26 = 560k
  - R31 = 10k
  - R32 = 820k

- **Capacitors:**
  - C1...C7 = 100n
  - C8, C9 = 33p
  - C10, C11 = 47n
  - C11, C12 = 47μF, 10 V
  - C12, C15 = 120p
  - C14, C16 = 10μF
  - C16, C17, C22 = 100n
  - C20 = 1μF ceramic
  - C23, C24 = 47μF, 16 V: tantalum
  - C25 = 220μF, 10 V

- **Semiconductors:**
  - D1, D2 = LED
  - D3 = BR212
  - IC1 = 4093
  - IC2 = 74HC04
  - IC4, IC6 = 4029
  - IC7 = 74L06
  - IC8 = 4511
  - IC9 = 4048
  - LD1...LD9 incl. = 7760 common-anode LED display
  - T1...T4 incl. = BC547B
  - TV = BC557B

- **Miscellaneous:**
  - L1 = Neodim inductor assembly Type 7F15
  - S1...S6 incl. = Digiwa switch (ITT or RT/THedow)
  - X1 = quartz crystal 11.2896 MHz
  - PCB Type 880165

---

**Fig. 8.** The completed pitch control board.

**Fig. 9.** Content of EPROM ICs.
It is also possible to alter the frequency of the oscillators driving the up-down counter to some extent. With the UP or DOWN key depressed, the reading on the display increases or decreases in steps. The rate of change of these steps is determined by time constant \( R \cdot C_5 \) or \( R \cdot C_6 \). Increasing the value of either the resistor or capacitor makes the reading change more slowly.

If the supply voltage comes on too slowly, it may be that the value of \( C_5 \) is too low for power-on-reset. Either the value of the capacitor or that of \( R \) may be increased to speed up the operation (\( R \) may be increased up to 100 kΩ).

The PCB in Fig. 7 may be cut into two to give separate synthesizer and display boards. It is then, for instance, possible to fit the display (as in the prototypes) into the CD players behind a small window to make frequent readings possible. It is, of course, also possible to construct the pitch control unit in a self-contained metal case and connect this to the CD player via as short a length of coaxial cable as possible. The case must be earthed to obviate external radiation of the 11 MHz clock signal.

---

**Fig. 10. Pitch control unit connected to one of the prototypes, a Philips Type CD960 CD player.**

**Fig. 11. Connection of the coaxial cable from the pitch control unit to the relevant board in the CD960.**

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**COMPUTER-AIDED TEST EQUIPMENT**

by A.W. Moore, MA

The (relatively) low cost, ease of use and flexibility of the personal computer make it eminently suitable for the control of test and measuring instruments. Many instrument and computer makers have realized this and have brought on to the market a number of parallel and serial buses to link a personal computer to one or more suitable instruments.

Not all that long ago, electronic equipment could be tested by the measuring of a few parameters (voltage, frequency, and so on) at some selected points in the circuit. Nowadays, much of such equipment is controlled by a microprocessor. Testing of this kind of equipment can only be carried out effectively by measuring the relevant parameters at many points in the circuit. Moreover, a number of these measurements needs to be taken simultaneously, owing to their interrelation.

With electronic equipment becoming more complex, instruments for testing such equipment have become more complex also and many are now controlled by a microprocessor. Such instruments are called automatic test instruments. If the internal microprocessor is controlled by an external computer, we speak of computer-aided test equipment.

Computer-aided test equipment may be dedicated, i.e., specifically designed and made for the relevant purpose, or it may consist of a PC controlling general-purpose instruments as shown in Fig. 2. A number of internationally well-known manufacturers, such as Philips, Hewlett-Packard, Tektronix, Schlumberger and Siemens have marketed dedicated computer-aided test equipment, but these are beyond the scope of this article.

If several instruments are to be controlled by a single PC, as in Fig. 2, it is an obvious advantage if a common bus is used. Such a bus makes the set-up very flexible since it allows extra instruments to be added without much trouble.
Buses used to link the various items in a test set-up should be of a standard design to enable instruments supplied by different manufacturers to interface. A number of standards has come about as a result of co-operation between various manufacturers, and some of them have been accepted by standards organizations, such as the IEEE and IEC.

There are parallel and serial buses, as well as Local Area Networks (LANs). Some buses are used for intra-board connection, such as the STD (IEEE961) bus, the VME (Versatile Module Europe) bus, and the Futurebus (IEEE896), whereas others are used for interconnecting instruments. Of the latter, the best known is the IEEE488. The IEC625 bus incorporates the IEEE488 standard, but uses a different connector. Local Area Networks are used to connect a variety of different terminals together over a given area.

The parallel intra-instrument buses are fundamentally compatible and are usually called general-purpose interface buses (GPIBs). A GPIB allows up to 16 instruments and a computer to be connected together. The instruments may be listeners (which can only receive data) or talkers (which can only send data). Many instruments manufactured nowadays are provided with a GPIB interface and switches that are used to set the bus address.

Sixteen active lines are used to implement the GPIB, and these are divided into three groups as shown in Table 1. The eight data lines are bidirectional and data is transferred byte by byte.

The control bus consists of five lines. When the ATN (attention) line is actuated (by the PC), it signifies to all instruments on the bus that they must give up use of the bus and interpret the data bus as commands. The IFC (interface clear) line is asserted by the PC and used to initialize the instruments. The REN (remote enable) line is used by the PC to instruct the instruments to be ready for remote control. The SRQ (service request) line is used by an instrument to interrupt the controller to signal that it requires attention. The EOI (end or identify) line is used to indicate the end of a multiple-byte transfer or, with ATN, to force the PC to execute a polling sequence.

The transfer control lines control the transfer of data on the data bus. The DAV (data valid) line is set by a talker to indicate that valid data are present on the data lines. The NDAC (not data accepted) line is set by a listener during reading the data. The NRFD (not ready for data) line is set by a listener to indicate that not all listeners are ready to accept data.

The IEEE488 standard does not define the syntax or code of messages on the bus.

### Table 1

<table>
<thead>
<tr>
<th>Pin</th>
<th>Data line</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>D&lt;sub&gt;1&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>D&lt;sub&gt;2&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>D&lt;sub&gt;3&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>D&lt;sub&gt;4&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>D&lt;sub&gt;5&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>D&lt;sub&gt;6&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>D&lt;sub&gt;7&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>D&lt;sub&gt;8&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Control line</td>
<td>EOI</td>
</tr>
<tr>
<td>10</td>
<td>IFC</td>
<td></td>
</tr>
<tr>
<td>11</td>
<td>SRQ</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>ATN</td>
<td></td>
</tr>
<tr>
<td>13</td>
<td>Screen</td>
<td></td>
</tr>
<tr>
<td>14</td>
<td>Control line</td>
<td>REN</td>
</tr>
<tr>
<td>15</td>
<td>Transfer line</td>
<td>DAV</td>
</tr>
<tr>
<td>16</td>
<td>NDAC</td>
<td></td>
</tr>
<tr>
<td>17</td>
<td></td>
<td></td>
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<td>18</td>
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<td>22</td>
<td></td>
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<tr>
<td>23</td>
<td></td>
<td></td>
</tr>
<tr>
<td>24</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The IEEE488 standard does not define the syntax or code of messages on the bus.

### Some typical available equipment

The Intepro Micro Series power supply test equipment from Limerick-based Intepro Systems is a PC expanded with a bus extender card that is complete with memory and capable of linking up to 255 plug-in instrument modules. Modules currently available include DVM, scanner, power relay, and ripple-and-noise measurement boards.

![Fig. 2. Typical GPIB structure.](image)

A full range of plug-in data acquisition and controls cards for IBM PCs and compatibles is available from Bleu Chip Technology.

Digital interface cards include the PIO-48 that has 48 programmable input or output lines. Other digital cards have optocouplers, Darlington drivers, relays and counter/timers.

Fig. 1. Typical computer-cum-instruments set-up.
The AIP-24, one of the analogue range, has 24 channels, 12-bit ana
gue-to-
digital converter, sample-and-hold, and a programmable gain amplifier.
Other cards include multi-function cards with analogue and digital channels, ther-
mocouple inputs and communications cards with RS232, RS422, RS485, and
20 mA standards.

ANALYSER software from Number One Systems is claimed to have become
the largest selling Circuit Analysis software package in Britain with versions for
the BBC and IBM (and compatible) PCs.
By simulating accurately the AC per-
formance of a circuit design, it can give
the designer confidence that circuits will
behave as required, without his needing
to resort to expensive test and measuring
equipment while "fine tuning" a design.
At higher frequencies, unanticipated ef-
fects can be caused by inter-electrode
capacitances and so on are immediately
made clear.

ANALYSER performs an AC Frequency
Response analysis on circuits entered
into the software, and presents results in
tabulated and graphical form. Analysis
of gain, phase, group delay, input im-
pedance, and output impedance versus
frequency are made to give the electronic
circuit designer a powerful tool with
which to assess the performance of
designs. Particularly useful is the ability
to change one or more component
values and recalculate to see the effects
of such changes. This allows
rapid solutions to design problems, and
minimizes the need for breadboarding
and the resultant waste of components
and, more important, time.

Strays and parasitics at higher fre-
quencies may also be taken into account.
ANALYSER allows resistors, capacitors,
inductors, transformers, field-effect and
bipolar transistors, operational ampli-
fiers, transmission lines and microwave
stripes to be included as circuit
elements. Circuits up to 60 nodes and
180 components may be analysed, and
there are libraries of active components
available that hold the pre-entered
specifications of up to 26 of each type
(bipolar transistor, FET, opamp). Data
may be changed by the user to suit the
types most commonly worked with.

Although not strictly a "computer-
aided" test equipment, Fieldtech's ORGANIZER II and COMMS LINK
are of interest to note.
The ORGANIZER II takes the place of
a PC as controller to drive IFR test in-
struments. Since the unit is little bigger
than multi-function calculator, it may be
used as hand-held controller that can be
stored in the test-set lid when not in use.

Fig. 3. PSION organizer radically changes RS-232 control & storage potential of IFR test instrum
ents.

Some useful addresses.

Ampicon Electronics Ltd
Richmond Road
BRIGHTON BN2 3SL
Telephone (0273) 68631

Blue Chip Technology
Main Avenue
Hamden Industrial Park
DEESEIDE
Clewrd CH5 3PP
Telephone (0244) 592222

Fieldtech Heathrow Ltd
Horniman House
410 Bath Road
LONGFORD UB7 0LL
Telephone 01-879 6446

Fluke Ltd
Colony Way
WATFORD WD2 4TT
Telephone (0923) 40511

Hewlett-Packard Ltd
Nine Mile Ride
WOKINGHAM RG11 3LL
Telephone (0634) 773100

Interpro Systems Ltd
Crescent House
77-79 Christchurch Road
KINGWOOD BH22 1DH
Telephone (0422) 474121

Keithley Instruments Ltd
1 Boulton Road
READING RG2 0NL
Telephone (0734) 801287

Number One Systems Ltd
Harding Way
Somersham Road

St. Pep
HUNTINGDON PE17 4WR
Telephone (0480) 61778

Philips Instruments
Mullard House
Torringtion Place
LONDON WC1E 7HD
Telephone 01-589 6633

Schlumberger Instruments
Victoria Road
FARNBOROUGH GU14 7PW
Telephone (0252) 544433

Siemens Ltd
Siemens House
Windmill Road
SUNBURG-ON-TIMES TW16 7HS
Telephone (0208) 85691

Tektronix Ltd
Fourth Avenue
Globe Park
MARLOW SL7 1YD
Telephone (0628) 6000
BACKGROUND TO E²PROMS

Memory chips with large storage capabilities invariably steal the limelight these days. There are, however, many interesting low-capacity devices available as well. One of these is the electrically erasable programmable read-only memory – E²PROM. Its low cost, versatility and ease of programming make this device an ideal component for many applications involving the permanent storage of, for instance, instrument configuration data.

As an example of the operation and application of a typical E²PROM (or EEPROM), this article discusses the 256-bit Type NMC9306 from National Semiconductor. Readers of this magazine will recognize this device from the Microcontroller-driven power supply (Ref. 1), where it is used for storage and retrieval of voltage and current settings associated with 3 user-selectable instrument configurations.

Basically, an E²PROM couples the non-volatility of an EPROM to the flexibility of a RAM. In this sense, it is functionally similar to a RAM with battery back-up, or a zero-power RAM (e.g. the 48202). Among the advantages of the E²PROM discussed here are its low cost and simple-to-use serial interface, which is of particular interest when the device is to be incorporated in existing systems.

Component availability note:
The NMC9306 is available from ElectroMail, P.O. Box 33, Corby, Northants NN17 9EL. Telephone: (0536) 204555. Stock number: 301-656.

Features and applications

An E²PROM is a read-only memory, and can, in principle, only be read from. Its special internal configuration, however, makes it possible to erase the device electrically, and re-load it, during normal operation. This obviates the need for exposure to ultraviolet light, and the application of a high programming voltage, required for erasing and programming a conventional EPROM. The NMC9306 is fed from a single supply voltage, 5 V, and has an on-chip step-up converter that supplies the programming voltage. Each of the sixteen 16-bit registers can be erased individually. An important difference with respect to a conventional RAM is, however, the time needed for loading (=writing to) a register. In the case of the NMC9306, this programming cycle takes at least 10 ms per register. Also, the number of write operations is limited to about 10,000 per register. The maximum guaranteed data retention period is 10 years, so that data will need to be ‘refreshed’ at least once during this time, by means of a erase-write cycle.

As already noted, the E²PROM is ideal for quasi-permanent storing of equipment configuration data. As an example of that application, Philips Test Instruments fit a number of their top-grade frequency meters with an E²PROM that holds data corresponding to the temperature response of the central quartz crystal built into a temperature-compensated oven. The temperature coefficient of each quartz crystal intended for use in these instruments is individually recorded as a curve, which is then digitized and loaded into the E²PROM. The microprocessor that controls the instrument measures the temperature of the oven, loads the relevant temperature coefficient from a look-up table, and corrects the central clock frequency to ensure minimum deviation.

Practical use

An essential difference between an E²PROM and other memory chips is

Fig. 1. Block and connection diagrams of E²PROM Type NMC9306.
apparent from the block diagram in Fig. 1. Data is sent to, and read from, the E'PROM via a serial interface, which not only makes it possible to house the chip in an 8-pin DIP package, but also makes its use independent of data- and address-bus structures—the E'PROM is simply a small peripheral device.

The serial input and output pins (DI and DO) may be controlled by separate serial formats. The serial interface is also used for reception, from the host microprocessor, of control commands for the E'PROM. These are 9-bit serial data words, in which the start bit is always logic high. The next 4 bits form the opcode (see Fig. 2), followed by another 4 bits that form the register address.

The function of the E'PROM control commands can be summarized as follows:

- **Read**: data is first loaded into the data shift-register, and then shifted out via the serial output DO. The shift-out operation is clocked by the low-to-high transition of the signal applied to the SK input. A dummy bit (logic 0) precedes the 16-bit data output string. Only the read instruction causes serial data to be output via the DO line.
- **Erase/write enable (EWEN)**: this command should always precede a data erase or loading operation.
- **Erase register**: unlike a RAM, an E'PROM register should be cleared (erased) before loading it with new data.
- **Erase all registers**: similar to the above command, but works on the whole chip rather than on an individual register.
- **Write**: load data in a previously cleared register.
- **Write all registers**: the same data is written to all registers.
- **Erase/write disable**: this command prevents accidental clearing or overwriting of registers.

### Table: Instruction set of the NMC9306 16×16-bit E'PROM

<table>
<thead>
<tr>
<th>Instruction</th>
<th>SB</th>
<th>Op Code</th>
<th>Address</th>
<th>Data</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>READ</td>
<td>1</td>
<td>10xx</td>
<td>A3A2A1A4</td>
<td></td>
<td>Read register A3A2A1A4</td>
</tr>
<tr>
<td>WRITE</td>
<td>1</td>
<td>01xx</td>
<td>A3A2A1A4</td>
<td>D15-D0</td>
<td>Write register A3A2A1A4</td>
</tr>
<tr>
<td>ERASE</td>
<td>1</td>
<td>11xx</td>
<td>A3A2A1A4</td>
<td></td>
<td>Erase register A3A2A1A4</td>
</tr>
<tr>
<td>EWEN</td>
<td>1</td>
<td>0011</td>
<td>xxxx</td>
<td>xxxx</td>
<td>Erase/write enable</td>
</tr>
<tr>
<td>EWCR</td>
<td>1</td>
<td>0000</td>
<td>xxxx</td>
<td>xxxx</td>
<td>Erase/write disable</td>
</tr>
<tr>
<td>EPAL</td>
<td>1</td>
<td>0010</td>
<td>xxxx</td>
<td>xxxx</td>
<td>Erase all registers</td>
</tr>
<tr>
<td>WRAL</td>
<td>1</td>
<td>0001</td>
<td>xxxx</td>
<td>D15-D0</td>
<td>Write all registers</td>
</tr>
</tbody>
</table>

NMC9306/CMOS has 17 instructions at least. Note that MSB of any given instruction is a '1' and is viewed as a start bit in the interface sequence. The next 4 bits carry the op code and the 4 for address for 1 of 16, 16×16 registers.

X is a don't care state.

#### Fig. 2. Instruction set of the NMC9306 16×16-bit E'PROM.

### Fig. 3. Timing of the E'PROM write cycle.

Two control lines on the E'PROM arrange the timing. Low-to-high clock transitions on line SK (serial data clock) control the shifting in and out of data and commands. The maximum clock frequency is 300 kHz. Line CS (chip select) is active high, and enables or disables all data and command I/O operations. It also serves to time the erase and programming pulses, which should have a duration of 10 to 30 ms. After the loading of a clear or write command, the relevant cycle starts when CS goes low. Programming lasts until CS reverts to logic 1. In the mean time, input SK is disabled. After programming has been completed, CS may remain logic high to enable loading a new command. When CS is made logic low, the E'PROM is switched to the low-power mode. In between commands, the minimum low-time of CS is 1 μs.

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**ELECTRONICS NEWS**

**SUPER TYPEWRITER**

An electric typewriter which can work up to a speed of 9,600 bauds per second will be brought out by Hindustan Teleprinter Ltd. HTL's new modems, which can work up to 2400 bauds per second, have been in-house R & D, has already been approved by DOT.

HTL achieved a record performance in 1987-88, with increased production and the emphasis was on new technology products. In the electronics typewriters, both Roman and bilingual. The company exceeded the target by 50 percent in electronic teleprinters, by 100 percent in electronic typewriters and 116 percent in modems.

**INDIAN SOFTWARE ATTRACTS EEC**

Ten Indian companies which recently demonstrated their software package to buyers in the European market received 100 inquiries, according to a report by the India Trade Centre at Brussels.

Indian companies showed their software alongside international giants like the IBM, Unisys and Honeywell. The Indian companies which were chosen for the exhibition included Ambalal Sarabhai Enterprises, CMC, Datamatix Consultants, Hindustan Computers, ICIM, Radix computers, Tata Consultancy Services, Wipro Information Technology and Blue Star. Most of the Indian companies found they were highly competitive both for products and services.

Meanwhile, another delegation comprising 18 members participated in COMDEX '88, America's biggest trade fair in software, microcomputers and peripherals.
Components, design ideas and application notes
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- Parametric equaliser
- Audio-analysing
- Audio-panel
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- Electronic signal-divider
- Alternative volume control
- Automatic volume limiter

Selex

- Charging/Discharging Current Meter
- Power Amplifier
- Mini-Synthesizer
- Baby phone

The megaphone
Tormentor
Switching transistor
The transistor – an electronic potentiometer
Humidity indicator for potted plants
Battery operated siren
Dim-Adapter
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- Car tilt alarm
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- Computer-controlled slide fader – 2
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- Discrete + 5 V to – 5 V converter
- Driver for bipolar stepper motors
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- Fast-starting wiper delay
- Fishing aid
- Flashing light
- Fruit machine
- Headlights indicator
- HF operation of fluorescent tubes
- High-voltage B25F
- Infra-red detector for alarm system
- Infra-red remote control for stepper motors
- Lead-acid battery charger
- Light-powered thermometer
- Overvoltage protection
- Power multivibrator
- Power switch for cars
- Programmable switching sequence
- Programmable voltage source
- Quiz timer
- Secondary power-on relay
- Self-switching power supply
- Servo-pulse generator
- Single-chip solid state relay
- Stepper motor driver
- Step-up switching regulator
- Switching mode power supply
- Timer
- Touch-sensitive light switch
- Ultrasonic distance meter
- Universal SMD to DIL adaptors
- Wiper delay
- Liquid crystal indicator
- Centimetre
- Rain Synthesiser
- Fuel-economiser
- Door bell memory
- Pulse relay
- Up/Down control for digital potentiometer
- Train detector
- Manual slide fader
- Computer or sensor controlled dimmer
- Synchroniser separator
- Water alarm
- Soft-start for halogen lamps
- Electronic mouse trap
- Two-wire remote control
- 9-Channel touch sensitive switch
- Fox hunt
- Light to frequency converter
- Giant LED display
- Deceptive car alarm
- Car lock defroster
- Motorphone
- Temperature controlled soldering Iron
AUTONOMOUS INPUT/OUTPUT CONTROLLER

A user-configurable I/O controller that gives digital and analogue interfacing power to your computer's RS232 outlet. Fast, simple to build and program, and intelligent enough to deal with up to 64 digital and 12 analogue channels, this microcontroller-driven I/O distribution box should prove invaluable in many applications where a computer runs a small or large-scale automated control job, be it industrial or domestic.

Part 1

The autonomous I/O controller described here is basically a versatile, intelligent, computer peripheral that can be connected into the bus structure proposed for the microcontroller-driven power supply published earlier this year (Ref. 1). Like the power supply, the I/O controller derives its intelligence from a Type 8751 microcontroller from Intel. The control program that resides in this chip has been written exclusively for this project in the Elektor Electronics design department.

Applications of the I/O box arise almost automatically when a computer is to communicate with the outside world. These applications range from essentially simple, such as the control of LED matrices, relays or electronic switches, to more sophisticated, interactive, ones including the control of motors, but also alarm, heating and air conditioning systems. The list of applications can be extended even further with PC-controlled battery chargers, light shows and audio distribution equipment. The 8-channel ADC in the system allows analogue values provided by a wide variety of sensors to be captured, stored and processed by the computer.

One button — seventy-six I/O lines

The basic operation of the autonomous I/O controller is best understood after looking at the front panel first (Fig. 1) — not a multitude of switches and other controls on this, just the on/off switch and a push-button labelled DISABLE OUTPUTS with an associated LED. There is no need for any other form of local operation, because the unit is controlled entirely by commands sent by the host computer it is connected to. There is nothing to look for at the rear side of the unit either: all that is there is the mains input socket and the 9-way D-connector that links the I/O box to the computer.

Part 2 of this article will detail the actual programming of the I/O controller with the aid of a set of commands similar to those used for the microcontroller-driven power supply. BASIC command PRINT (or LPRINT) is perfectly adequate for sending these commands via the RS232 port, so that even beginners need not worry about bus interfacing, machine language programs, or the intricacies of the microprocessor inside the host computer. Most computers provide some sort of printer output redirection facility, so that the use of the RS232 port obviates the need for complex programs to 'talk and listen' to the peripherals connected to the I/O box. There is, of course, a price to be paid for all these benefits, and this is mainly the limited speed of the system. None the less, 9600 baud should be fast enough for any of the applications mentioned earlier, since the minimum pulse duration that can be programmed on a digital output line is about 6 ms.

Three printed circuit boards

Figure 2 shows that the autonomous I/O controller can be expanded to user requirements. The system is in principle composed of 3 types of sub-unit:

- controller board — this holds the microcontroller, power supply and the 10-bit analogue-to-digital converter (ADC) with its associated 8-channel input multiplexer;
- bidirectional digital board — this is identical to that for the 8052AH-BASIC computer (see Refs. 2 and 3);
- analogue output board — this is virtually identical to that for the 8052AH-BASIC computer (see Refs. 2 and 3).

There is a slight difference to note between the autonomous I/O controller and the system discussed in Ref. 3. This difference entails the maximum number of peripheral boards (digital and analogue output). In the autonomous I/O controller, there may be 0, 1, 2, 3 or 4 boards of each type, provided each is allotted a unique address (this will be reverted to in Part 2). It is not allowed to replace, for example, two analogue output boards with two bidirectional I/O boards, or the other way around.

Push-button DISABLE OUTPUTS provides a toggle function for simultaneously switching on and off all digital outputs. The current state of this function is indicated by a LED.
A further LED, labelled REMOTE CONTROL lights when the autonomous I/O controller communicates with the host computer.

An interesting and original feature offered by the system described here is its ability to interconnect pairs of corresponding input and output lines with the aid of software (command 'G'). A practical application of this feature is shown in Fig. 5 where a pair of I/O lines is used with manual switch control.

**On the controller board**

As already noted, the controller board holds the 'brains' of the system, the microcontroller Type 8751, and the ADC with its associated 8-way input multiplexer. The circuit diagram is given in Fig. 3.

Since the basic operation of the microcontroller, IC1, is briefly covered in Refs. 1 and 4, the device can be treated as a 'black box' that takes care of the serial communication, the control of peripherals (digital I/O, DACs and ADC), the multiplexing of the analogue inputs, and the timing for the I/O latches. The microcontroller has on-chip RAM and ROM.

Circuit IC2 is a supply monitor chip that ensures the correct initialization of the microcontroller at power-on. It also works as a watchdog, checking the presence of 11 ns long pulses on controller output line P2.0. When these pulses fail, the microcontroller is immediately reset. This is done to prevent the system generating uncontrolled signals when the supply voltage drops below the level needed for correct operation, or when the system 'hangs up' due to some internal malfunction. CPU port line P2.0 is also fed to the bidirectional digital boards. Conflicts with the watchdog are avoided by the microcontroller ensuring that WR is never activated when a pulse is sent to the watchdog chip.

Diodes D1 and D2 determine the address, or identification code, assigned to the autonomous I/O controller — see Table 1. With 2 diodes, a choice of 4 addresses is available. This will do for most applications, given the large number of lines provided by a single autonomous I/O controller.

<table>
<thead>
<tr>
<th>D1</th>
<th>D2</th>
<th>Value</th>
<th>Bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>o</td>
<td>o</td>
<td>144</td>
<td>16</td>
</tr>
<tr>
<td>f</td>
<td>o</td>
<td>146</td>
<td>16</td>
</tr>
<tr>
<td>o</td>
<td>f</td>
<td>148</td>
<td>16</td>
</tr>
<tr>
<td>f</td>
<td>f</td>
<td>150</td>
<td>16</td>
</tr>
</tbody>
</table>

or if

<table>
<thead>
<tr>
<th>Value</th>
<th>Bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>145</td>
<td>16</td>
</tr>
<tr>
<td>147</td>
<td>16</td>
</tr>
<tr>
<td>149</td>
<td>16</td>
</tr>
<tr>
<td>151</td>
<td>16</td>
</tr>
</tbody>
</table>

or if

<table>
<thead>
<tr>
<th>Value</th>
<th>Bits</th>
</tr>
</thead>
<tbody>
<tr>
<td>o</td>
<td>16</td>
</tr>
<tr>
<td>f</td>
<td>16</td>
</tr>
</tbody>
</table>

Accuracy, the reference voltage should be as high as possible, but it must never exceed the supply voltage. The reference voltage is, therefore, set to +5 V supplied by the well-known precision stabilizer Type REF-02 (IC3), and the supply voltage to +5.25 V, supplied by an LM317 (IC6). The voltage difference of 0.25 V is a safety margin that should prevent fluctuations on the output voltage of the LM317 damaging the ADC.

The +5 V, -5 V and -12 V power supplies on the controller board are of conventional design and merit no further discussion.

The operation of the serial interface will be discussed in Part 2, as part of the software command descriptions.

**INPUT/OUTPUT MODULES:**
- Modular structure. Largest system configuration supports:
  - 32 digital outputs;
  - 32 digital inputs;
  - 4 analogue outputs;
  - 8 analogue inputs.
- Digital interface card has 8 buffered outputs and 8 protected inputs. Up to 4 of these modules can be used in I/O system.
- Analogue output card has 1 output with 10-bit resolution. Output voltage span: 0 to +10.23 V, programmable in 10 mV steps. Up to 4 of these modules can be used in the I/O system.
- Analogue-to-digital converter on controller board has 8 multiplexed inputs. Input voltage span: 0 to +10.23 V. Resolution: 10 mV/LSB.
- Medium-power open-collector digital outputs are surge-protected, and can handle 50 V, 500 mA loads directly.
- Optional internal connection of digital inputs and outputs.
- Ideal for multitasking of peripherals on a single serial computer channel.

**PROGRAMMING AND SERIAL INTERFACE:**
- Standard serial interface and data format allow system to be controlled by almost any microcomputer or terminal. Simple line settings:
  - 9600 bits/s; 2 stop bits; no parity bit.
  - Line settings and selective addressing of peripherals is compatible with microcontroller-driven power supply. Up to 4 autonomous I/O controllers can be individually addressed via a single serial channel.
  - Communication with or without echo.
  - Status control codes provided for host computer.
  - All functions are programmable via serial interface.
  - Programmed output voltages are read on analogue outputs; real output voltages can be read from the output
  - Digital output lines are individually programmable, or in blocks of 8 bits.
  - Analogue output voltages are individually programmable.
  - Automatic syntax-checker for control commands.

![Fig. 2. Modular structure of the autonomous I/O controller.](image-url)
Fig. 3. Circuit diagram of the controller board, which holds the micromicrocontroller, the A-D converter, and a number of peripheral chips.
Analogue-to-digital conversion
The 8 analogue inputs on connector K1 are connected to protective diode-resistor networks. The CPU, IC1, controls the ADC direct, and the input multiplexer, IC2, via 4 level converters, T1 to T4. The INH (inhibit) input of the Type 4051 CMOS analogue multiplexer, in combination with capacitor C4 and opamp IC4, makes it possible to realize a basic sample-and-hold function. C4 is dimensioned such that it provides an acceptable compromise between rise and full time — the conversion error it introduces is less than 1/2LSB. Potential divider R3-R6 scales the sampled analogue voltage down to a value between 0 and 5 V.

The analogue inputs form a high impedance when they are not sampled. When they are, the impedance drops to about 10 kΩ. The procedure for loading and conversion to 8 bits of the 10-bit data in ADC Type ADC1005 is largely similar to that adopted for the Type DAC1006 (for details, see Ref. 3). An important feature of the ADC1005 is its insensitivity to current peaks during the actual conversion process, as well as to occasional negative voltages supplied by opamp IC4. No attempt should be made to suppress the current peaks by fitting a capacitor at the input of the ADC, since this would result in significant conversion errors.

Bidirectional digital card and analogue output card
The circuit diagrams of these modules are given in Figs. 4 and 6 respectively. For a description of the operation, refer to Ref. 3 (but note the value of R3 on the analogue voltage board, and the supply voltages). The address assignment can be deduced from Table 2. The digital I/O cards can only be addressed by fitting jumpers E0 to E3 (on K1), the analogue output boards by fitting jumpers E4 to E7 (also on K1). Do not swap cards of a different type.

<table>
<thead>
<tr>
<th>K3</th>
<th>Peripheral module</th>
</tr>
</thead>
<tbody>
<tr>
<td>E0</td>
<td>digital card 0</td>
</tr>
<tr>
<td>E1</td>
<td>digital card 1</td>
</tr>
<tr>
<td>E2</td>
<td>digital card 2</td>
</tr>
<tr>
<td>E3</td>
<td>digital card 3</td>
</tr>
<tr>
<td>E4</td>
<td>analogue card 0</td>
</tr>
<tr>
<td>E5</td>
<td>analogue card 1</td>
</tr>
<tr>
<td>E6</td>
<td>analogue card 2</td>
</tr>
<tr>
<td>E7</td>
<td>analogue card 3</td>
</tr>
</tbody>
</table>

Construction
The printed circuit boards for building the autonomous I/O controller are shown in Figs. 7 (controller board; double-sided, through-plated), 8 (digital I/O board) and 9 (analogue output board). The 26-way flat-ribben cable
that ‘buses’ connectors K1 of the digital and analogue boards connect these to the controller board. Construction of the controller board should not cause difficulty. Note that all electrolytic capacitors are radial types that are fitted upright. Component R2 is an 8-way, 9-pin, single-in-line (SIL) resistor network. Make sure that the protective diodes are fitted the right way around (D1 to D2: cathode up; D3 to D4: cathode down). The 5 V regulators may be fitted on to the cabinet side panel with the aid of insulating washers. It is recommended to fit supply decoupling capacitor C0 at the track side of the board, straight across pins 20 and 40 of the microcontroller.

The photograph in Fig. 11 shows the prototype of the autonomous I/O controller fitted in an enclosure of the same size as that used for the microcontroller-driven power supply. There is plenty of space left for fitting two mains transformers (a single type that provides approximately 9 V at 0.8 A, and 15 V at 250 mA, may be difficult to obtain).

The drawing of Fig. 10 and the ready-made, self-adhesive, front-panel available for this project are used as templates for preparing the aluminium front panel of the enclosure. Remember to drill recessed holes for the counter-sunk screws that secure the D-type sockets and anything else attached to the inside of the front panel, such as horizontal support pillars between this and the rear panel. Small additional holes are drilled in the front panel as shown in Fig. 10 to give access the multiturn presets on the analogue output boards (these holes are not provided in the self-adhesive front panel foil, and must be punched after carefully lining up the completed analogue boards behind the aluminium front panel). A sharp hobby knife is used for clearing the holes for the sub-D connectors in the foil.

Adjustment of the analogue output board is carried out as described in Ref. 3. The board with identification...
Fig. 8. Printed circuit board for the digital I/O board.

Fig. 9. Printed circuit board for the analogue output board.

Fig. 10. Front panel drilling template.
number \( n \) is programmed to provide 10.00 V with the aid of instruction

Un,10.00.

The ADC on the controller board is calibrated by applying a precision voltage of 10.00 V and adjusting \( P_1 \) until the host computer reads exactly this value. Details on programming the autonomous I/O controller will be given in next month's final instalment.

Finally, note that the logic ground and the analogue ground are interconnected at one point only, close to the ADC1005.

Fig. 11. Internal view of the prototype.

GUIDING THOSE WAVES

by W.D. Higgins

An increasing number of engineers have to consider processing signals in the gigahertz frequency range: satellite TV, information/data systems, point-to-point microwave links, and radar are but a few examples of fields where a basic understanding of the operation of waveguides is required, and where this brief guide to waveguides may prove useful as an introduction.

A waveguide is essentially a precision-engineered length of hollow, usually rectangular, aluminium, invar, copper or brass (70/30 and 90/10) tubing that serves to carry microwave RF signals. Whereas professional-grade coaxial cable is used up to about 3 GHz with considerable attenuation, certain types of waveguide are suitable for carrying RF signals at frequencies of 50 GHz and higher, at an insertion loss that remains negligible even for relatively long runs. Waveguide technology can be treated as a very fine art, but is in principle very similar to conventional plumbing. Since waveguides and ancillaries such as coupling flanges, preformed twists, T-junctions and coaxial transitions are available ready-made in a variety of sizes, the engineer will have to decide on the most appropriate practical size of the waveguide, bearing in mind cost and machinability. To these factors must be added the technical consideration whether or not a particular waveguide size can be used at the frequency of interest. The physical size of a waveguide determines the lowest frequency at which it can be used, i.e., at which it is capable of propagating RF energy in a relatively loss-free manner. Any type of waveguide, therefore, has its specific cut-off frequency, below which attenuation rises rapidly.

The dominant propagation mode in a waveguide is referred to as \( TE_{10} \). The distribution of the electric and magnetic field in \( TE_{10} \) mode is illustrated in Fig. 1. The electric field strength is maximum at the centre of long walls of the waveguide, and decreases sinusoidally towards the short walls. The magnetic field has a loop-like configuration, and is distributed in parallel with the long wall of the waveguide.

To prevent excessive attenuation, the \( TE_{10} \) mode requires a minimum size of the internal waveguide width, \( w \), of 0.5\( \lambda \). The previously mentioned cut-off frequency therefore corresponds to a wavelength, \( \lambda \), equal to 2\( w \). Width \( w \) should not exceed \( \lambda \) to prevent the dominant mode changing from \( TE_{10} \) to another electromagnetic pattern whose
structure causes matching problems at the input and output of the waveguide. In practice, \( w \) is made slightly greater than 0.5\( \lambda \) because the wavelength of a signal in a waveguide, \( \lambda_g \), is greater than the free-space wavelength, \( \lambda_0 \):

\[
\lambda_g = \frac{\lambda_0}{\sqrt{1 - (\lambda_0/2w)}}
\]

This equation applies to the TE\(_{10}\) mode, and shows that \( \lambda_g \) approaches infinity as \( w \) approaches 0.5\( \lambda \). In practice, the minimum value of \( w \) is chosen between 0.6\% and 0.95\% to prevent components or joints in the waveguide causing propagation discontinuities or electrical losses. Similarly, to prevent polarization reversal between the input and output of the waveguide, the internal height, \( h \), is chosen lower than 0.45\( \lambda \). The maximum frequency of operation of a waveguide is usually 2\( f_c \).

**Standard range**

Most manufacturers of precision waveguides produce a standard range of sizes (and materials) that conforms to various European and US specifications. European specifications include IEC153 (1&2), DIN47302, BS9220, DEF5351 and CCTU10-20. US specifications include MIL-W-85C, EIA, RS261-A and JAN-MIL.

Waveguide size is denoted by a WG number. The most commonly used sizes are in the range WG5 to WG28 — the higher the WG number, the smaller the waveguide, and the higher the cut-off frequency (remember that this is the lowest frequency at which the waveguide can be used). Table 1 gives data of a number of waveguide sizes.

As a rule of thumb, the attenuation of a waveguide increases with length and the WG number. A few examples of typical attenuation figures are included in Table 1. WG16 is particularly popular among radio amateurs for use in 3-cm (X-band) and home-made Ku-band equipment (satellite TV reception). Military waveguide systems are often offered in a variety of configurations at rallies, and by electronic surplus stores. Often, such units come complete with associated SHF electronic parts, such as Gunn-diodes, klystrons, adjustable attenuators, mixer diodes and even horn aerials. Waveguide circulators also exist, but are hard to get hold of.

The usual way of joining lengths of waveguide is by means of flanges. These are slipped over the waveguide and then brazed or soldered in place. Excess waveguide is usually milled or filed away. Great care should be taken to keep the inside of the waveguide free from

---

**Fig. 1.** Relative intensity of the electric and magnetic component in TE\(_{10}\) mode.

**Fig. 4.** A piece of WG16 waveguide fitted with one flange, a small horn aerial, and a home-made two-stage Ga-As FET preamplifier for Ku-band satellite TV reception, fitted on to a length of brass waveguide. Input and output coupling to the waveguide is effected with internal \( \frac{1}{4}\lambda \) probes.
residual solder, as this introduces high losses. In general, discontinuities smaller than 0.1λ are tolerable, so that it is perfectly possible to make one’s own waveguide (and even flanges) from available brass or aluminium tubing. Waveguide tee-pieces, adjustable matching pieces, cross-couplers, dummy loads, tunable filters, coax adaptors, twists and bends, flexible connecting pieces and directional couplers are available for most types of waveguide. Factors to consider when joining lengths of waveguide, or inserting connectors in a waveguide system, include the frequency range, VSWR of all ports, power division, port-to-port RF isolation, phase balance, power handling, polarization and, of course, physical parameters.

**Future trends**

As greater use is made of the microwave bands, the demand for waveguides, and with it SHF research and development, is found to increase. In the field of metallurgy, new alloys may be invented with better physical characteristics, to reduce attenuation, improve machinability, and allow greater power handling.

**Addresses of companies handling waveguides:**

- **Du-Ken (c/o Frequency Techniques**
- **Cornwallis House**
- **Howard Chase**
- **Basildon**
- **Essex SS14 3BB. Telephone: (0268) 293401.**

---

**Table 1.**

<table>
<thead>
<tr>
<th>WG number</th>
<th>Inside dim. (mm)</th>
<th>Outside dim. (mm)</th>
<th>Attenuation (dB/m)</th>
<th>Frequency range (GHz)</th>
<th>Cut-off frequency (GHz)</th>
<th>Weight (kg/m)</th>
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<td>73.8</td>
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</tr>
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**Fig. 2.** Not a decapitated robot, this, but a 4-port phase shifter for new high-power C-band radars currently under development in the USA and Sweden. Photograph courtesy of MM Microwave.

**Fig. 3.** WG-16 waveguide input of a low-noise block down converter (LNC) for Ku-band satellite TV reception.
**LFA-150: A FAST POWER AMPLIFIER (FINAL PART)**

from a basic idea by A. Schmeets

**Protection circuit.** The protection circuit serves to:
- delay the energizing of the output relay by a few seconds from power-on;
- on switch-on, monitor the d.c. resistance of the loudspeaker: if this is lower than 2.2 ohms, the output relay is not energized;
- deactuate the output relay if the direct voltage across the output terminals of the amplifier rises above 1 volt;
- deactuate the output relay if the peak current flowing in the output transistors rises above 10 A;
- deactuate the output relay if one, or both, of the secondary a.c. voltages fails—this also ensures that the loudspeakers are disconnected from the output when the amplifier is switched off.

The circuit diagram of the protection unit is shown in Fig. 9. Note, however, that the output relay and the peak-current detector are located on the current-amplifier board.

The 24-V output relay is actuated by T₆₉ and T₆₃. These transistors form a Schmitt trigger, so that the relay is actuated when the potential across C₇ has risen to about 12 V and is de-energized when that voltage has dropped to about 6 V. The hysteresis is determined by R₉₉ and R₉₆.

Inverter T₄₂ in the collector circuit of T₆₉ conducts when the protection circuit is on, and this causes D₉₉ to light. When the power is switched on, C₇ charges via R₇₇. Once the potential across the capacitor has reached a value of about 12 V, T₆₉ begins to conduct. Transistor T₆₉ is then switched on and the output relay is energized.

Capacitor C₇ is shunted by transistor T₆₈, which enables it to discharge very rapidly if a fault arises. The base circuit of the transistor is connected to a poten-
Fig. 10. Printed-circuit board for the protection unit.

Parts list

**PROTECTION BOARD**

**Resistors (±5%):**
- R12, R13, R14 = 15 kΩ
- R15, R16 = 100 kΩ
- R17, R18 = 2 kΩ
- R19, R20 = 1 kΩ
- R21 = 47 kΩ
- R22 = 1 kΩ
- R23 = 10 kΩ
- R24, R25 = 470 kΩ
- R26, R27 = 1 kΩ
- R28 = 330 kΩ
- R29, R30 = 100 kΩ
- R31, R32 = 15 kΩ
- R33 = 18 kΩ
- R34 = 27 kΩ
- R35 = 3 kΩ
- R36, R37 = 1 kΩ
- R38 = 56 kΩ
- R39 = 1.2 kΩ
- R40 = 150 kΩ
- R41 = 270 kΩ
- R42, R43, R44 = 4.7 kΩ
- R45 = 15 kΩ
- R46 = 1 kΩ
- R47 = 33 kΩ
- R48 = 63 kΩ
- R49 = 390 kΩ
- R50 = 470 kΩ

**Capacitors:**
- C1 = 0.1 μF, 25 V
- C2, C3 = 1 μF, 63 V
- C4, C5 = 2200 pF, 25 V
- C6 = 100 μF, 40 V
- C7 = 47 μF, 63 V
- C8 = 47 μF, 63 V

**Semiconductors:**
- D1, D2, D3 = 1 N4148
- D2, D3 = 1 N4002
- D4 = 33 V, 0.4 W zener diode
- T1, T2, T3, T4 = BC5566
- T5 = BF256A
- T6 = BC539
- T7 = T8, T9, T10 = BC5366
- I = LF411CN

**Miscellaneous:**
- K = 10-way header for PCB mounting.
- Two IDC sockets to mate with K1 and K2.
- PCB Type 880902.3

The power supply for IC1 is derived from the ±56-V lines via zener diodes D2 and D3 and series resistors R30 and R31. The direct voltage at the output of the amplifier is measured by the differential amplifier formed by Transistor T5 and T6. The output signal is fed to T5 via potential divider R30-R31, and to T6 via a bipolar electrolytic capacitor formed by C4 and C5. The difference signal across the collector of the transistors is applied to low-pass section R30-R31-C4-C5. If the d.c. voltage is greater than ±1 V, the collector voltage of either T5 or T6 drops to such an extent that T6 is switched on via D3 or D4 and this causes the relay to be de-energized via T4. The d.c. operating point of the difference amplifier is set with the aid of constant current source T7. The current is about 2.5 mA.

Transistors T5 and T6 in the current amplifier measure the peak voltage across the emitter resistor of one of the output transistors in the positive and negative half of the output signal respectively. The voltage dividers in the base circuit of T5 and T6 are dimensioned to cause the transistors to conduct when a peak current of 2 A flows through the output transistors. In that case, T5 switches on T22, or T5 switches on T23 in either event. T6 is switched on (via D3 or D4) and de-energizes the output relay, so that the loudspeaker is disconnected.

Power for the protection circuit is taken direct from the ±56 V lines, but power supply monitoring diodes D30 and D31 are connected to the secondary winding of T4 (40 V a.c.).

All other connections between the protection circuit and the amplifier are made via connector K3.

The PCB for the protection circuit is shown in Fig. 10. Populating this is not likely to present any problems.

**Ancillary PSU board.** This board, shown in Fig. 11, is intended to house the auxiliary transformer, Tr, rectifiers D30-D31, and smoothing capacitors Cs and C6. The board is designed to be fitted with a number of terminal blocks to facilitate the inter-wiring of the amplifier sections. Make sure that the smoothing capacitors are rated at 100 V.

**Construction.**

The construction details are given for a mono amplifier; two are, of course, needed for a stereo amplifier. The heat sink must be at least 170 x 80 mm and be drilled in accordance with Fig. 8. Its resistance must be not greater than 0.5 K/W. The holes are made with a 2.5 mm drill and then tapped to receive 3 mm machine screws.
The enclosure used for the prototypes measures 245 x 120 x 300 mm; for a stereo amplifier, a larger enclosure is needed. A small section must be cut from its rear panel to make space for the heat sink (see Fig. 1). The heat sink is mounted at a height that allows fitting the AF input sockets underneath it. The mains input and loudspeaker terminals are located beside it.

Mains transformer $T_2$, rectifier $R_1$, and the ancillary PSU board are mounted on the base panel of the enclosure. The board for the protection circuit may be mounted on top of the voltage amplifier as shown in Fig. 7 or, alternatively, at another convenient place in the case. The mains on-off switch and the power, error, and low impedance diodes are mounted on the front panel.

All components in the current amplifier, except $T_3$ to $T_5$ incl., are fitted at the track side of the board a few millimeters above the surface.

Inductor $L_1$ consists of 12 turns 1.5 mm thick enamelled copper wire on a hollow former of roughly 15 mm diameter. Resistor $R_{13}$ is inserted into the centre of the former and the whole assembly is fitted on the board in one go, again a few millimeters above the surface.

Seven solder pins and a 10-way connector are used for the remaining connections with the other sections of the amplifier.

The terminals of $T_3$, $T_4$, and $T_5$ are bent upwards 90° about 3 mm from their housing. The transistors are then screwed to the heat sink with the aid of insulating washers with the terminals upwards. It should then be possible to fit the current amplifier board on four 10-mm spacers with the transistor terminals protruding through the appropriate holes in the board (see Fig. 5).

Next, the terminals of the output transistors are bent as shown in Fig. 13. These four transistors are then fitted on to the heat sink with the aid of insulating washers and discs, and plenty of heat conducting paste. Take care that the correct washers and discs are used, because the transistors have different cases. The terminals should coincide with the appropriate solder areas on the board.

All transistor terminals may now be soldered to the board.

Transistor pairs $T_1$-$T_4$; $T_2$-$T_5$; $T_3$-$T_4$; and $T_5$ should preferably be matched. If that is not feasible, they should come from the same production batch (normally indicated on their body).

Pairs $T_1$-$T_2$ and $T_4$-$T_5$ are mounted on the board with their smooth sides adjoining. Some heat conducting paste should be applied between each pair, after which the pairs should be tightened together with a nylon cable tie. This is done to ensure that the two transistors in each pair have the same temperature and so prevent their d.c. operating from shifting.

The other two pairs are mounted on an L-shaped piece of aluminium, after which the whole assembly (see Fig. 14) is fitted on to the board with the aid of two short spacers. Insulating washers and heat conducting paste should be used in the construction. Solder pins for connections $A$, $B$, $C$, and $FB$ should be fitted at the track side.

When the board is populated, it may be mounted on top of the current amplifier board with the aid of four 35-40 mm spacers.

The mains input plug should preferably be of the type with built-in fuse. From there, a length of mains cable goes to the on-off switch on the front panel.

Another length of mains cable goes from the on-off switch to the auxiliary PSU board and $T_2$.

Make sure that mains-carrying cables and parts are at correct isolating distances from other parts.

The power supply section is wired in accordance with Fig. 12. Note that the secondary (40 V) voltage is applied separately to the auxiliary PSU board. The only earth point of the enclosure is wired to the central connection of the 20,000 µF electrolytic capacitors.

Check whether the two mains transformers are connected in series by switching on the mains and verifying that the voltage at the ±70-V terminals is about 70 V with respect to earth. If the voltage is lower, for instance, 45 V, switch off the mains and interchange the two primary connections of $T_3$ on the auxiliary PSU board. Again switch on the mains and check the voltage at the ±70-V terminals. When everything is in order, discharge the electrolytic capacitors carefully with the aid of a 470-ohm, 1-watt resistor.

Solder short lengths of (enamelled) cop-
Fig. 12. Inter-wiring diagram for the various sections of the amplifier.

1.52 élektor india January 1989
per wire between points A, B, C, and FB on the voltage amplifier and current amplifier boards.
Connect the input socket to the input of the voltage amplifier by a short length of screened cable.
Connect the power input terminals on the current amplifier board to the take-off points on the electrolytic capacitors by 2 mm thick insulated copper wire. Use similar wire for the connections to the output terminals.
The supply terminals on the voltage amplifier board are connected to the 70-V terminals on the ancillary PSU board. The protection board is connected to the current amplifier board via a length of 10-way flat cable terminated at both ends into a suitable 10-way connector. Make sure that pin 1 of the protection board is connected to pin 1 of the current amplifier board.

Setting up
Set P1, P2, and P3 to the centre of their travels, and P4 to maximum resistance. Switch on the mains supply. After a few seconds, the direct voltages at C1+ and C2− should be +58 V w.r.t. earth.
Adjust P1 and P2 to obtain voltages of ±60 V across R17 and R18 respectively. Adjust P3 to obtain a direct voltage of exactly 0 V at the junction L1−R41−T11.
Adjust P4 to obtain a voltage of 20 mV across R20 and across R24. This voltage indicates a current of around 90 mA through each output transistor, which ensures trouble-free Class A operation.

Fig. 13. Mounting of the output and driver transistors on the heat sink.

Fig. 14. Construction of the heat sink for T3−T11.
COLOUR TEST PATTERN GENERATOR

from an idea by G. Kleine

A PAL-compatible colour video source that supplies a number of test patterns for aligning television sets.

A test pattern generator is virtually indispensable for troubleshooting in television sets because it supplies a video signal that is known to be stable, and thus easily displayed and synchronized on an oscilloscope. Moreover, the instrument allows the user to trace a fault in a TV set or other video equipment by selecting the most appropriate test pattern (e.g. a cross-hatch for convergence testing, or a dot pattern for focusing adjustment).

The test pattern generator discussed here is based on three integrated circuits: a pattern generator (ZNA234E from Ferranti), a video matrix chip with DAC inputs (LM1886) and an associated video modulator (LM1889). The latter two chips are manufactured by National Semiconductor.

**Block diagram**

The general set-up of the pattern generator is shown in Fig. 1. In principle, all patterns originate from the ZNA234E, which supplies the luminance information for a dot pattern (DOT), a cross-hatch pattern (XH), a horizontal line pattern and a vertical line pattern. The vertical bar pattern supplied by the chip is not used here because it is unsuitable for generating a colour staircase signal — this is derived from the vertical line pattern.

The output signal supplied by the pattern generator circuitry is monochrome, i.e., it contains only luminance information. Colour is obtained by applying the luminance signal to one or more inputs of the RGB generator. RGB signals are fed via two switches to a colour matrix. The first switch selects between the vertical bar pattern and the other patterns. The second switch disables the colour burst and thus allows the colour staircase to be made monochrome, i.e., to be converted to black, white and intermediary shades of grey. The other patterns can be viewed in black and white also by turning on red, green and blue simultaneously. The monochrome

![Fig. 1. Block diagram of the test pattern generator.](image-url)
colour switch provides the three 3-bit D-A inputs of the colour matrix with an RGB signal whose composition results in 8 colours or 8 shades of grey.
In the colour matrix, the RGB signal is translated into the corresponding levels for the luminance and chrominance component. Colour coding is essentially to the PAL (phase alternation line) standard. The LM1899 combines the signals supplied by the matrix with that of the colour burst generator. The composite video signal thus obtained is available at a buffered output. An RF modulator on board the LM1899 modulates the composite video signal plus a 1 kHz audio test tone on to a carrier in the VHF-I band (approx. 48 to 65 MHz; now no longer used in the UK). An external UHF modulator is required for testing TV sets tuned to channels in the UHF band. The pattern generator provides a 625-line picture.

Circuit description
The circuit shown in Fig. 2 is not nearly as complex as it looks at first sight. In fact, it is fairly simple, and merely a combination of smaller sub-sections, whose basic function has been discussed above.
Circuit IC1 provides the pattern signals and two synchronization signals, mixed sync (MS) and mixed video blanking (MVB). The switch that feeds the patterns to the colour generator is formed by IC5, an 8-to-1 multiplexer. Actually, the circuit referred to as 'colour generator' is composed of three NAND switches, N5, N6 and N7. The vertical bar pattern is generated by counter IC6.
Circuit IC12 is the vertical bar/pattern switch.
The monochrome/colour switch built around IC16, IC14 drives the video matrix, IC18, and the modulator, IC19.
The 1 kHz test tone oscillator set up around T3. This is switched on and off by the logic level at the Q2 output of IC16 (0 = off; 1 = on). Preset P1 is adjusted for optimum stability of the oscillator.
The burst oscillator on board the LM1899 operates at the PAL chrominance subcarrier frequency, 4.433 MHz, with the aid of an external quartz crystal and a capacitor network.
The user interface of the test pattern generator is formed by push-button switches S1 to S4. Each of these controls a function with the aid of a JK bistable (FF1 to FF4). Key debouncing is

Fig. 2. Circuit diagram of the test pattern generator.
achieved with a combination of a Schmitt-trigger gate (N\textsubscript{s} to N\textsubscript{t}) and an R-C network. The logic level at outputs Q and Q of each bistable toggles every time the associated key is pressed. LEDs connected to the bistable outputs show the currently selected mode of the pattern generator.

The pattern generated by the circuit is selected by S\textsubscript{5}, whose debounced pulses clock counter IC\textsubscript{s}. An auto-repeat function is provided on S\textsubscript{5}. The least significant bit supplied by the counter controls the 1 kHz AF oscillator, so that each pattern is available with or without a test tone. The three most significant counter bits control the pattern selector, the vertical bar/pattern switch, the monochrome/colour switch, and the pattern indicator formed by IC\textsubscript{s} and indicator LEDs D\textsubscript{6} to D\textsubscript{8}.

Before the function of the control signals in the circuits is discussed, it is useful to examine the operation of the colour switch.

The simplified diagram of Fig. 3a shows the configuration of the six toggle switches (IC\textsubscript{15} to IC\textsubscript{18}) between IC\textsubscript{15} and IC\textsubscript{18}. When 'colour' is selected, each basic colour has only two shades (saturation minimum or maximum), since the inputs of each DAC are interconnected. For test purposes, this arrangement still results in enough colour combinations. The switch configuration for 'monochrome' is shown in Fig. 3b. The inputs are connected such that only one 'colour', white, is available, but the intensity can be controlled to give grey and black — the RGB information applied is simply used as a 3-bit luminance (Y) signal.

Returning to the control circuitry of the test pattern generator, D\textsubscript{1}, D\textsubscript{2}, and R\textsubscript{2} provide an OR function that controls the vertical bar/pattern selector, IC\textsubscript{15}. A logic high level supplied by D\textsubscript{1} selects the RGB signal from gates N\textsubscript{15} to N\textsubscript{18}; a logic low level, the signal from the vertical bar generator. The fourth switch contact in IC\textsubscript{15} controls the monochrome/colour selector. When IC\textsubscript{15} is set to 'pattern', IC\textsubscript{15} and IC\textsubscript{16} are set to the 'colour' position (note that this does not exclude a monochrome picture, since red plus green plus blue gives white). When IC\textsubscript{15} is set to 'pattern', the monochrome/colour and colour selection depends on the logic level at the Q\textsubscript{4} output. When this is low, IC\textsubscript{15} and IC\textsubscript{16} are in the 'colour' position, so that the colour bars are generated. A high level at Q\textsubscript{4} selects the staircase signal for monochrome applications.

As already noted, the bar pattern (monochrome as well as colour) is derived from the signals supplied by IC\textsubscript{3}. The pattern is basically generated by the luminance signal for the vertical line pattern, which is composed of a number of pulses at fixed intervals in each line. These pulses are used for clocking a 4-bit counter. Since the three most significant bits function as RGB outputs, the colour obtained changes with every second pulse applied to the clock input. The RGB information remains the same in between two pulses, so that a coloured bar is obtained. Between two lines IC\textsubscript{15} is reset by the inverted MVB signal (N\textsubscript{7}) to ensure that the counter has the same start state (nought) at each line. Signals burst enable, B\textsubscript{E}, H/2, and a bias signal are combined with the available RGB signals in IC\textsubscript{3}. The chrominance subcarrier generated by IC\textsubscript{3} is applied to the bias input, pin 7, of IC\textsubscript{15}. The other two signals, B\textsubscript{E} (burst key) and H/2, are obtained with the aid of monostables MM\textsubscript{1} to MM\textsubscript{4}, and bistable FF\textsubscript{3}. Signal H/2 is the line toggle signal that inverts the R-Y signal for each line in the PAL picture. FF\textsubscript{3} is synchronized by MM\textsubscript{4} to

![Image](image_url)
ensure that the temporarily doubled horizontal sync-pulse rate in the vertical sync interval does not upset the PAL timing.

The Y-output of ICs carries a colour CVBS (composite video, blanking, synchronization) signal. The photographs in Fig. 4 show oscillograms of one picture line in the colour bar pattern (Fig. 4a), and one line of the monochrome staircase pattern (Fig. 4b). The CVBS signal is buffered by an amplifier around T2 to T3, to enable driving a 75 Ω load.

Unfortunately, the RF modulator contained in the LM1889 can only operate at VHF Band 1 channels (2 to 4). Vestigial sideband suppression is not provided — the RF spectrum generated is simply that of a DSB (double-sideband) modulator. The frequency of oscillation is determined by an external L-C tank circuit, Cn-Cn-Ln. The modulator is driven with the CVBS signal and the FM sound carrier, whose frequency is set to 6.0 MHz (UK) or 5.5 MHz. Like the chrominance and RF carrier oscillators, the sound subcarrier oscillator is also contained in ICs. Frequency modulation is achieved with the aid of varicap D1, which forms part of an external L-C tuned circuit, Ln-Cn-Cn.

Switching between a colour and monochrome picture is effected by pressing the BURST ON/OFF button (toggle function). The quartz-crystal controlled chrominance subcarrier oscillator is disabled when T1 conducts.

**Construction**

The printed circuit board for the test pattern generator, shown in Fig. 5, is a double-sided, but not through-plated, pre-tinned type with a large ground plane at the component side to keep digital interference within limits. All integrated circuits are fitted on to the PCB without IC sockets. In some cases, component terminals (including IC pins) are soldered at both sides of the board to effect through-contacting.

Commence the construction with installing 11 short pieces of through-contacting wire in the vicinity of the boxed EPS number at the component side. Mount and solder one component at a time, and check that pins or terminals, where appropriate, are soldered at both PCB sides. Use a soldering iron with a fine tip.

RF inductor L2 consists of 6 turns of 1 mm dia. (SWG20) enameled copper wire. The internal diameter is about 6 mm. Space the turns evenly so that the wire ends can enter the holes provided. The photograph of the prototype in Fig. 6 shows that the RF section of the circuit is screened with 20 mm high tin.

**Faultfinding in TV sets**

A test-pattern generator is a video signal source intended for locating malfunctions in TV sets and video equipment. Below are a number of possible applications of the instrument described in this article.

**Convergence:**

Convergence is the intersection, at a specific point at the inside of the multibeam picture tube, of the R (red), G (green) and blue (B) electron beams. Convergence errors are usually observed as beam divergence in the picture corners, where an originally white, single, line diverges in two or more, colour, lines.

Required test-patterns: cross-hatch or vertical lines.

**Focusing:**

Focusing and convergence adjustments usually interact, but may use different circuits in the TV set. An improperly focused picture appears blurred and hazy. Like convergence, focusing may have to be optimized with the aid of separate adjustments that work on parts of the screen.

Required test-patterns: dots, cross-hatch and vertical lines.

**RGB amplifiers:**

Given that the picture tube still has equally active R, G and B emitters, these should have closely matched DC amplification characteristics to prevent colour distortion.

Required test-pattern: colour bar (luminance of individual colours decreases: white, yellow, cyan, green, magenta, red, blue, black).

**Uniform saturation on whole screen:**

The background colours without a test-pattern enable checking for uniform colour saturation in all areas of the screen. Light spots point to ageing effects in the picture tube.

**Burst:**

A monochrome picture is generated when the colour burst is turned off. This ability of the pattern generator may be useful for troubleshooting the colour demodulator and chrominance circuits.
Fig. 5. Component mounting plan of the double-sided printed circuit board.

Inductors:
L1 = 10 µH
L2 = home-made inductor; see text

Miscellaneous:
S1 = double-pole mains switch in mains entrance socket with built-in fuseholder.
S2 = Se incl. = push-to-break button.

X1 = 5 MHz quartz crystal in HC18 enclosure.
X2 = 4.433619 MHz quartz crystal in HC18 enclosure.

BNC socket.
Common heat sink for IC1 and IC2.
Fuse: 100 mA delayed action.
Mains transformer 15 V; 400 mA.
Metal enclosure: approx. size 26 x 8 x 18 cm.
TV coax socket.
PCB Type 880130
plate sheets, which are joined in the corners, and soldered on to the ground plane. The screen that runs along IC\textsubscript{6} and IC\textsubscript{8} can only be soldered in the corners and close to Cu because of the tracks running beneath it. Trimmer C\textsubscript{9} is to be mounted a few millimetres above the board surface to prevent overheating of the PTFE foil when the two rotor connections are soldered to the ground plane.

Finally, on ready-made board 880138, connect pins 1, 2, 3 and 14 of IC\textsubscript{13}, and pin 1 and 2 of IC\textsubscript{11}, to ground.

**Setting up**

The video part of the test pattern generator is fairly simple to adjust. Connect a colour monitor with a 75 \(\Omega\) CVBS input to the corresponding output of the circuit. Set all trimmers to the centre of their travel, and turn the wiper of P\textsubscript{1} to ground. Press the BURST ON-OFF key when D\textsubscript{14} lights. Carefully adjust trimmer C\textsubscript{3} for minimum interference between the coloured bars.

As already noted, the VHF modulator is only for use with a TV set on which VHF Band I is available.

Tune the TV to, say, channel 3 (in Europe, TV channel E3 = 55.25 MHz). Adjust C\textsubscript{3} until the test pattern appears. If available on the TV set, use the fine adjustment to obtain a clear picture; otherwise, carefully adjust C\textsubscript{3} with an insulated trimming tool. Turn up the volume on the set and adjust C\textsubscript{9} for minimum AF noise. This tunes the sound oscillator to the correct subcarrier frequency (6.0 MHz or 5.5 MHz, depending on the country you live in). Press S; if D\textsubscript{14} does not light, carefully advance P\textsubscript{1} until the test tone is heard in the receiver. Increase the frequency and turn the volume by tuning P\textsubscript{1} up to a point where the tone becomes steady. Turn the wiper back until the steady tone is restored at maximum volume.

**Fig. 6.** The completed board (prototype). Note the screening around the RF sections.

**Fig. 7.** Suggested front panel layout.

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**VIDEO & TV—WHICH IS BETTER?**  
**Continued from page No. 1-18**

aged. All these 50 to 70 systems could be looked after by one or two motivated, unemployed educated youth who could be paid a good salary of Rs. 2,000 per month and motorbike. The same person could also be responsible to handle the software cassettes and circulating them among the villages. A library of 1,000 video cassettes in local language will cost only Rs. 75,000 without any government concession. The ET & T is already selling the 45-minute cassette for Rs. 75. A proper rationalisation of the exercise and Customs duty could enable the distribution of cassettes for still lower prices. The cost of running such a programme for 25 villages over a period of two years will not exceed Rs. 10 lakhs. Of this, 80 per cent of the investment is by way of capital cost. The real cost including depreciation will be a mere Rs. 5 lakhs in two years or Rs. 2.5 lakhs a year. This means the cost per village per year will be Rs. 10,000. For this price, each village will get three complete community video systems, each consisting of 20-inch colour TV and a video player.

There is salvation even for villages without power. The ET & T has developed a very low cost DC battery-to-main convertor which could run the combination VCP-TV. A community video set can be operated with a 12 V car battery. Two methods of charging this battery has also been developed. One of these is a system involving stationary bicycle kind of arrangement which drives the dynamo charging the battery. The cost is not more than Rs. 2,000. The other solution is charging through solar panels. In this system, the battery can be charged for the entire year.
COMPOSITE - TO - TTL ADAPTOR FOR MONOCHROME MONITORS

Among the welcome side-effects of the current invasion of IBM PCs and compatibles are the drastic price cuts for high-resolution, 12 and 14 inch, TTL-compatible monochrome monitors. The circuit described here makes it possible to use such a display in conjunction with a computer that has a composite video output only.

Many owners of popular home computers must at some time have been envious of IBM PC users, because these are in a position to look at text and graphics on a restive, high-resolution, non-glare monitor instead of on a (modified) TV set tuned to channel 36, and barely capable of displaying 80 characters per line. Until recently, however, the cost of a TTL monitor was such that manufacturers of home computers in the lower price ranges did not even consider equipping them with a digital output. The inexpensive adaptor circuit described here should allow many owners of the first generation of home computers to benefit from the advantages offered by the TTL-compatible monitor.

A fast comparator, based around opamp Type 733 (IC1) and FETs T1-T3, extracts the video component from the CVBS input signal. It should be noted that the attainable contrast ratio is mainly determined by the speed of the opamp, so that the circuit does not work correctly if IC1 is replaced by a slower type. The toggle point of IC3 is set to the average video level by P1. Impedance conversion between the opamp and the digital video input of the monitor is achieved with T4 and T5, the latter functioning as an adjustable zener diode.

Construction, setting up and application

The adaptor is constructed on the printed circuit board shown in Fig. 3. The two inductors are preferably ferrite-encapsulated radial types from Toko. The completed unit can be installed in the monitor, which usually has room to spare inside. This has the advantage that the adaptor can be fed from the existing power supply, ensuring correct interface levels (check the specification of the monitor in this respect). As shown in the circuit diagram, the adaptor is uncritical of the supply voltage level, as long as this is between 5 and 12 V, and well regulated.

An oscilloscope enables the unit to be aligned quickly. With reference to Figs. 2 and 3, measure the levels \( \frac{1}{2} U_{\text{sync}} \) (X), and \( U_{\text{sync}} + \frac{1}{2} U_{\text{video}} \) (Y), and set these voltages as the toggle levels for IC2 (P1) and IC3 (P2) respectively. Adjust P3 for optimum picture resolution and stability. When an oscilloscope is not available, set P3 and P2 to the centre of

---

Circuit description

The circuit shown in Fig. 1 effectively splits the CVBS (composite video-blanking-synchronisation) signal applied to the input into three components: horizontal and vertical synchronisation pulses, and video. These three signals are then converted to digital level to enable driving the corresponding inputs on the TTL monitor.

The low reference level of the CVBS signal is first set to 0 V by an active clamping circuit around IC1. Figure 2 shows the voltage levels in a CVBS signal. Note that the amplitude of \( U_{\text{sync}} \) is usually about one third of that of \( U_{\text{video}} \). The switching threshold of comparator IC2 is set such that only the synchronisation pulses can cause the opamp output to go low. The composite sync signal is then fed to XOR gate N1 and to a two-section R-L-C low-pass filter. Switch S1 connected to pin 2 of N1 selects the signal polarity at the H-sync output. The presence there of V-sync pulses has no consequence for the TTL monitor. The V-sync pulses obtained after filtering in the low-pass can be inverted, if necessary, by closing S1. Inversion is probably not necessary for most types of monitor, but users are well advised to consult the relevant manual in case of doubt.

---

Fig. 1. Circuit diagram of the composite-to-TLL converter.
Fig. 2. Toggle level for the sync comparator (X) and for the video comparator (Y).

Fig. 3. Oscilloscope display of one line of text in a monochrome CVBS signal supplied by a BBC model B computer.

their travel, and turn the wiper of P1 to ground. Apply the input signal, and carefully advance P1 until the picture synchronizes. Then adjust the other two presets for optimum picture quality, first P2 and then P3.

The circuit is dimensioned to work with input video levels between 1 Vp and 4 Vp. The value of R17 may have to be increased, or the resistor may have to be omitted, to ensure correct operation with home computers whose output level is lower than 1 Vp. Signal levels exceeding 4 Vp can be accommodated by lowering the value of R2. Capacitor C1, finally, also allows some experimenting because it may not be required unless a very high resolution monitor (>80 characters per line) is being used.

Fig. 4. Printed circuit board for the adaptor.

**Parts list**

<table>
<thead>
<tr>
<th>Resistors (±5%)</th>
<th>Capacitors</th>
<th>Semiconductors</th>
</tr>
</thead>
<tbody>
<tr>
<td>R1 = 10K</td>
<td>C1: C2 = CA3130</td>
<td>D1 = 1N4148</td>
</tr>
<tr>
<td>R2, R6, R11, R13 = 1KΩ</td>
<td>C3: C6 = 33n</td>
<td></td>
</tr>
<tr>
<td>R3 = 470R</td>
<td>C3, C5 = 2n2</td>
<td></td>
</tr>
<tr>
<td>R4, R12 = 2K2</td>
<td>C4, C10 = 47n</td>
<td></td>
</tr>
<tr>
<td>R5 = 220K</td>
<td>C7 = 390p</td>
<td></td>
</tr>
<tr>
<td>R6 = 1K5</td>
<td>C9 = 4μ7, 16 V</td>
<td></td>
</tr>
<tr>
<td>R7 = 2K7</td>
<td>C11 = 100n</td>
<td></td>
</tr>
<tr>
<td>Rg, R10 = 10K</td>
<td></td>
<td></td>
</tr>
<tr>
<td>R14, R15 = 100R</td>
<td></td>
<td></td>
</tr>
<tr>
<td>R16 = 120R</td>
<td></td>
<td></td>
</tr>
<tr>
<td>R17 = 220R</td>
<td></td>
<td></td>
</tr>
<tr>
<td>P1 = 500R preset H</td>
<td></td>
<td></td>
</tr>
<tr>
<td>P2 = 2K or 2K5 multiturn preset</td>
<td></td>
<td></td>
</tr>
<tr>
<td>P3 = 50K preset H</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Miscellaneous:

L1, L2 = 10mH radial inductor, e.g. Toko Type 181LY-103 (CIRKIT stock no. 34-10302).
S1, S2 = miniature SPST switch.
PCB Type 860059.
LOOKING BACK

Updates, applications and improvements for recently published projects

Stereo limiter
(Elektor India, February 1988, p. 2.41.)

The operation of this design can be improved with a few minor alterations, which have to do mainly with the DC bias of the gain cells in IC1. To begin with, C1 and C10 are replaced by wire links. This upsetting the DC bias of A5 and A6, however, so that further modifications are required. The positive (non-inverting) inputs are taken to pin 6 and pin 10 of IC1 instead of to ground. Further, R-C networks are fitted across R15 and R16 to reduce the direct voltage gain to about unity. The R-C networks only provide negative feedback for direct voltage, and do not, therefore, affect the AC gain. The last modification entails connecting an electrolytic capacitor in series with R8 (R16). The modified circuit diagram of the stereo limiter is given in Fig. 1.

On the printed circuit board, replace C10 and C16 by wire links. The R-C networks are soldered directly across R8 and R16. Remove IC1 from its socket and bend up pins 3 and 5 before re-inserting the chip. Use short lengths of insulated wire to connect pins 3 and 5 with the indicated pins of IC1.

HF operation of fluorescent tubes
(Elektor India, July 1988, p. 7.41.)

Control of more than one tube.
As stated in the article, the controller is, in principle, suitable for powering one tube only. When two tubes are connected in parallel, a problem arises during starting. Normally, when one tube is connected, resonance will occur at some point when the VCO frequency swings from 80 kHz to 30 kHz, and it is at this point that the tube is started. With two tubes in parallel, one will always start first, causing damping of the resonance circuit and making it impossible for the other to start. Simultaneous starting of the tubes is possible, but a matter of pure chance. Moreover, the current control

Fig. 1. Modified circuit diagram of the stereo limiter.
circuit and power output stage of the tube controller are not capable of handling double the current. Series connection of fluorescent tubes offers better prospects, but works only with relatively low-power tubes of up to 2 × 20 W. The connection diagram is shown in Fig. 2. Capacitor C1 is omitted from the board, and 'split up' in C1a and C1b. During starting, C1a and C1b ensure a current flow through all tube filaments, and at the same time provide equal distribution of the start voltage. Since C1a and C1b are connected in series, their value should be double that of C1 (see Table 1 in the article) to give the correct equivalent capacitance. Two series-connected fluorescent tubes of 20 W each are now equivalent to a single 40 W tube.

Following the simultaneous ignition of the tubes, these can be dimmed as if they were one tube. It will be noted, however, that the point of minimum brightness (set with P3) is slightly less favourable than with one tube. This is so because at a certain point one tube will go out, but its parallel capacitor will tend to keep the other on. This effect can be explained by the highly irregular impedance characteristic of the fluorescent tube, which behaves like a current-dependent resistance. Series connection of fluorescent tubes is best done with types of the same manufacturer, wattage and age.

There is no way to go round building the required number of HF controller boards when connecting, for instance, two tubes of more than 30 W, or 4 tubes of 20 W. Fortunately, these can still be dimmed simultaneously with a central control as shown in Fig. 3. In this set-up, it is important that the mains connections to the controller boards are in phase.

Cable length between controller and tube.

A cable of several metres length is, in principle, no problem as long as its capacitance is low relative to that of C1. In practice, this means that cables from K1 should not be allowed to run too close to those from K2. It is still strongly recommended to fit the controller board as close as possible to the tube, with adequate ventilation, because the use of a relatively high switching frequency on a long cable is bound to introduce a strong electromagnetic field which causes radio and TV interference. The use of shielded wire, however, is not recommended because it increases the capacitance to ground.

Oscillator stability.

The bias current of zener diode D12 in the control circuit is relatively low to reduce the current consumption of the control circuit. In some cases, the bias current is too low, however, and gives rise to instability of the zener voltage. This results in temperature dependence of the oscillator start frequency. To ensure reliable start behaviour of the circuit, it is recommended to readjust a number of components:

- $R_{se}$ is changed from 6K8 to 2K7;
- $R_{se}$ is changed from 39K to 15K;
- $C_{e}$ is changed from 100 $\mu$F to 220 $\mu$F.

Fig. 2. One controller board connected to two fluorescent tubes.

Fig. 3. Showing how controller boards can share the intensity control potentiometer. A timer/controller for aquarium lighting is currently under development.
LOGIC FAMILIES COMPARED

by Pete Chown

A brief look at the most important characteristics of recently introduced logic families, and the way in which they can be interfaced to one another.

Today, there exists a bewildering variety of logic families, and the rate at which new families are introduced and older ones become obsolete is perplexing to many. Metal-gate CMOS and standard 74 series TTL are now reaching the end of their useful life. Low power Schottky (LS) TTL is often still the first choice, although this family is now being superseded by HC-MOS. The continued use of LS and S (high-speed) TTL probably results from lack of information about the alternatives. It is not enough to say that LS TTL does the job, however, because alternatives offer reduced power consumption.

The reason for the existence of so many different types of logic integrated circuit is that there is always a trade-off between speed and power consumption. The graph in Fig. 1 shows speed plotted against power. The modern logic families are those nearest to the bottom left, the point which would represent the ideal logic device, offering instantaneous operation at a power consumption of nought. The devices shown in the graph tend to form a line moving between the axes, showing different trade-offs between speed and power. The older devices, LS, 74, 4000 and S, appear above this line. Among the new families is ALS-TTL, Advanced Low-power Schottky, offering devices which are faster and more economical as regards power consumption than pure LS-TTL versions.

High-speed CMOS

The new 74HC and 74HCT series of silicon-gate CMOS devices offer speeds equivalent to LS-TTL, but with negligible power consumption. The 74HC device is the most useful, as it consumes least power, and offers the best range of output voltages for driving external devices (maximum output low voltage \( V_{OL} = 0.1 \) V; maximum output high voltage \( V_{OH} = 4.9 \) V). The problem comes with their inputs. It is here that HC and HCT devices are different. Although \( \pm 2.4 \) V might seem a strange value to mean logic high, standard 74 series TTL can give exactly that in the worst case. This level is, however, outside the specifications for HC devices. IC Manufacturers have been aware of this, and have developed HCT devices by changing the inputs of HC types, so that the worst-case TTL logic high level will be accepted. Full compatibility of HCT with LS-TTL is thus achieved at the cost of a small increase in the power consumption.

HC and HCT devices are excellent for applications where low power and high noise immunity are important design considerations. The quiescent current consumption of an HC-MOS gate is about 0.0025 µW, increasing to about 170 µW at 100 kHz. Silicon-gate CMOS is the superior family when a high fan-out is required, since one output can drive about 1000 inputs. Many devices in the 74 LS-TTL family can practically be replaced by corresponding HCT devices as pin-compatible replacements. It is easy to become over-excited about the very fast devices, although these will probably have far more impact on us in years to come, probably becoming what LS-TTL is today. We can, however, look forward to getting rid of noisy fans, and to lifting the lid of our PC without the usual blast of hot air.

There are two device families that fall between the ones discussed. These are the 74AS/74ALS series and the FAST series. These two families are really rivals from different manufacturers — ALS is made by Texas Instruments, and FAST by Fairchild and Motorola. The 74AS and 74ALS series offer a substantial reduction in power consumption over the 74S and 74LS series respectively. The fan-out is doubled, propagation delays have been considerably reduced, and the maximum bistable frequency has been increased to 200 MHz.

Fig. 1. Speed-power relationships of a number of commonly used logic families.
Interfacing logic families

One of the reasons that designers have been reluctant to use the new logic families is that they are worried about interfacing these devices to existing circuits. The rules for interfacing are quite simple. Many devices are designed to be compatible without any external device. Most others simply need a resistor. The overview in Table 1 gives information on interfacing a number of logic families. The actual value of the pull-up resistor (when required) is chosen to lie roughly between the low value and the high value, which are calculated as follows:

\[ R_{\text{low}} = \frac{Vcc - V_{OL,\text{max}}}{I_{OL} + \frac{n}{I_{IH}}} \]  \[ R_{\text{high}} = \frac{Vcc - V_{HI,\text{min}}}{nI_{IH} - I_{OH}} \]

where
- \( Vcc \) = supply voltage;
- \( I_{OL,\text{max}} \) = maximum output low voltage;
- \( I_{OH} \) = maximum sink current of driving device;
- \( n \) = number of device inputs being driven;
- \( I_{IH} \) = input current to driven device when input is low.

It will be found that NMOS does not normally need a resistor because this would have a very high value. Not surprisingly, therefore, circuits work well without one. Table 2 lists some commonly used logic families and their parameters, allowing resistance values to be worked out. The resistor should obviously be inserted pulling up to \( Vcc \). To choose the correct values to use in the above formulae, take the output parameters for the driving gate, and the input parameters for the driven gate.

**Conclusion**

If you think the logic market is complex now, it will be even more so in a few years' time, because gallium-arsenide (Ga-As) devices promise operating speeds of around 4 GHz. These new devices will be around in parallel with EACT (Fairchild Advanced CMOS Logic) and existing TTL for a good time, because they will initially be so expensive. The ACT family, like HCT, is fully LS-TTL compatible, while AC gives basically the same drive problems as HC. Both new series are typically 2 to 3 times faster than LS-TTL or HCMOS. It should be noted that AC and ACT devices have a different supply pinning than LS-TTL, while the number of logic functions currently available is limited to certain bus drivers, and encoders/decoders. The range of AC/ACT devices is expected to extend considerably, however, in the next year or so.

Good documentation is essential for anyone designing, analyzing and testing circuits based on devices from the new logic families.
CORRECTIONS

Preamplifier for purists
November 1988 p. 11-37

In the top drawing of Fig. 4, the 47 pF capacitor across R31 should be labelled C26, not C24. In the lower drawing, the 47kΩ resistor to the left of R31 should be labelled R37.

The accompanying diagram shows the corrected component mounting plan of busboard 1 (Fig. 6 on p. 33). Note the placement of points M and N, and the connections between the tuner input terminals and points E and F.

Macrovision decoder/blanker
November 1988 11.48

The ISSUE LED, D5, may fail to light even when a video signal of sufficient amplitude is applied. This can be resolved by replacing the Type 7805 voltage regulator in position IC4 with an 7805 or 7809, which have the same pinning. The use of an 7805 requires R14 to be increased to 15 kΩ.
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